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Instruction Sequences with Dynamically Instantiated Instructions*

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Abstract. We study sequential programs that are instruction sequences with dynamically instantiated instructions. We define the meaning of such programs in two different ways. In either case, we give a translation by which each program with dynamically instantiated instructions is turned into a program without them that exhibits on execution the same behaviour by interaction with some service. The complexity of the translations differ considerably, whereas the services concerned are equally simple. However, the service concerned in the case of the simpler translation is far more powerful than the service concerned in the other case.

Keywords: instruction sequence, dynamically instantiated instruction, projection semantics, program algebra, thread algebra, action transforming use mechanism

1. Introduction

In this paper, we study sequential programs that are instruction sequences with dynamically instantiated instructions. With that we carry on the line of research with which a start was made in [3]. The object pursued with this line of research is the development of a theoretical understanding of possible forms of sequential programs, starting from the simplest form. The view is taken that sequential programs in the simplest form are sequences of instructions. Program algebra, an algebra of programs in which programs are looked upon as sequences of instructions, is taken for the basis of the development aimed at.

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The approach to define the meaning of programs followed in this line of research is called projection semantics. It explains the meaning of programs in terms of known programs instead of more or less sophisticated mathematical objects that represent behaviours of programs under execution. The main advantage of projection semantics is that it does not require a lot of mathematical background. Over and above that, the view is taken that the behaviours of sequential programs under execution are threads as considered in basic thread algebra [3].¹ Therefore, the meaning of the programs considered in program algebra is explained in terms of threads. The experience gained so far leads us to believe that sequential programs are nothing but linear representations of threads.

Sequential programs in the form of assembly programs up to and including sequential programs in the form of structured programs are covered in [3]. However, although they are found in existing assembly programming practice, indirect jump instructions are not considered. In [5], several kinds of indirect jump instructions are considered, including a kind by which recursive method calls can easily be explained. Dynamic instruction instantiation is a programming feature that is not suggested by existing programming practice. However, from the viewpoint that sequential programs are nothing but linear representations of threads, it is a genuine programming feature. It is a useful programming feature as well, as will be illustrated by means of an example in the paper. Therefore, we consider a theoretical understanding of instruction sequences with dynamically instantiated instructions relevant to programming.

We believe that interaction with services provided by an execution environment is inherent in the behaviour of programs under execution. Intuitively, some service provides for dynamic instruction instantiation. In this paper, we define the meaning of programs with dynamically instantiated instructions in two different ways. In either case, we give a translation by which each program with dynamically instantiated instructions is turned into a program without them that exhibits on execution the same behaviour by interaction with some service. In one case, the service concerned provides in effect for the dynamic instruction instantiation and, in the other case, it is largely achieved by the translated programs. We consider it useful to treat both cases because of the considerable difference in complexity of the two translations.

A thread proceeds by doing steps in a sequential fashion. A thread may do certain steps only for the sake of having itself affected by some service. The interaction between behaviours of programs under execution and some service referred to above is an interaction with that purpose. In [7], the use mechanism is introduced to allow for such a kind of interaction between threads and services. In this paper, we will use a generalization of the use mechanism, called the action transforming use mechanism, to have behaviours of programs under execution affected by services. This generalization is reminiscent of the state operator introduced in [1].

A hierarchy of program notations rooted in program algebra is introduced in [3]. In this paper, we embroider on one program notation that belongs to this hierarchy. The program notation in question, called PGLD, is a very simple program notation which is close to existing assembly languages. The hierarchy also includes a program notation, called PGLS, that supports structured programming by offering a rendering of conditional and loop constructs instead of (unstructured) jump instructions. Each PGLS program can be translated into a semantically equivalent PGLD program by means of the projection semantics of PGLS and some intermediate program notations.

¹In [3], basic thread algebra is introduced under the name basic polarized process algebra. Prompted by the development of thread algebra [7], which is a design on top of it, basic polarized process algebra has been renamed to basic thread algebra.

This paper is organized as follows. First, we review basic thread algebra, program algebra, and program notation PGLD (Sections 2, 3, and 4). Next, we provide a simple classification of services that will be used in subsequent sections (Section 5). After that, we extend basic thread algebra with the action transforming use mechanism and introduce a state-based approach to describe services (Sections 6 and 7). Then, we give a state-based description of a service that can provide for dynamic instruction instantiation and use that service to define the meaning of the programs from a variant of the program notation PGLD with dynamically instantiated instructions (Sections 8 and 9). Following this, we introduce a concrete notation for basic instructions that covers dynamically instantiated instructions and use that notation to illustrate the usefulness of dynamic instruction instantiation (Section 10). Thereupon, we give a state-based description of a register file service and use that service to define the meaning of the programs from the variant of the program notation PGLD with dynamically instantiated instructions in another way (Sections 11 and 12). Finally, we discuss the semantic approaches followed in the preceding sections and make some concluding remarks (Sections 13 and 14).

2. Basic Thread Algebra

In this section, we review BTA (Basic Thread Algebra), a form of process algebra which is concerned with the behaviours that sequential programs exhibit on execution. The behaviours concerned are called *threads*. BTA was first presented in [3]. In that paper, as well as several other papers, BTA is called BPPA (Basic Polarized Process Algebra).

In BTA, it is assumed that there is a fixed but arbitrary finite set of *basic actions* \mathcal{A} with $\text{tau} \notin \mathcal{A}$. We write \mathcal{A}_{tau} for $\mathcal{A} \cup \{\text{tau}\}$. The members of \mathcal{A}_{tau} are referred to as *actions*.

A thread performs basic actions in a sequential fashion. The intuition is that each basic action performed by a thread is taken as a command to be processed by a service provided by the execution environment of the thread. The processing of a command may involve a change of state of the service concerned. At completion of the processing of the command, the service produces a reply value and returns that reply value to the thread. The reply value is either \mathbf{T} or \mathbf{F} and determines how the thread proceeds.

Although BTA is one-sorted, we make this sort explicit. The reason for this is that we will extend BTA with an additional sort in Section 6.

The algebraic theory BTA has one sort: the sort \mathbf{T} of *threads*. To build terms of sort \mathbf{T} , BTA has the following constants and operators:

- the *inaction* constant $\mathbf{D} : \mathbf{T}$;
- the *termination* constant $\mathbf{S} : \mathbf{T}$;
- for each $a \in \mathcal{A}_{\text{tau}}$, the binary *postconditional composition* operator $-\triangleleft a \triangleright - : \mathbf{T} \times \mathbf{T} \rightarrow \mathbf{T}$.

Terms of sort \mathbf{T} are built as usual (see e.g. [16, 17]). Throughout the paper, we assume that there are infinitely many variables of sort \mathbf{T} , including x, y, z .

We use infix notation for postconditional composition. We introduce *action prefixing* as an abbreviation: $a \circ p$, where p is a term of sort \mathbf{T} , abbreviates $p \triangleleft a \triangleright p$.

The thread denoted by a closed term of the form $p \triangleleft a \triangleright q$ will first perform a , and then proceed as the thread denoted by p if the processing of a leads to the reply \mathbf{T} (called a positive reply) and proceed

Table 1. Axiom of BTA

$$\frac{x \triangleleft \text{tau} \triangleright y = x \triangleleft \text{tau} \triangleright x \quad \mathbf{T1}}{\quad}$$

Table 2. Axioms for guarded recursion

$$\frac{\langle X|E \rangle = \langle t_X|E \rangle \quad \text{if } X = t_X \in E \quad \mathbf{RDP}}{\quad}$$

$$\frac{E \Rightarrow X = \langle X|E \rangle \quad \text{if } X \in V(E) \quad \mathbf{RSP}}{\quad}$$

as the thread denoted by q if the processing of a leads to the reply F (called a negative reply). The action tau plays a special role. It is a concrete internal action: the processing of tau will never involve a state change and always lead to a positive reply, but notwithstanding all that its presence matters. The threads denoted by D and S will become inactive and terminate, respectively.

BTA has only one axiom. This axiom is given in Table 1. Using the abbreviation introduced above, axiom $\mathbf{T1}$ can be written as follows: $x \triangleleft \text{tau} \triangleright y = \text{tau} \circ x$.

Each closed BTA term of sort \mathbf{T} denotes a finite thread, i.e. a thread that will become inactive or terminate after it has performed finitely many actions. Infinite threads can be described by guarded recursion specifications.

A *guarded recursive specification* over BTA is a set of recursion equations $E = \{X = p_X \mid X \in V\}$, where V is a set of variables of sort \mathbf{T} and each p_X is a term of the form D , S or $p \triangleleft a \triangleright q$ with p and q BTA terms of sort \mathbf{T} that contain only variables from V . We write $V(E)$ for the set of all variables that occur on the left-hand side of an equation in E . We are only interested in models of BTA in which guarded recursive specifications have unique solutions, such as the projective limit model of BTA presented in [2]. A thread that is the solution of a finite guarded recursive specification over BTA is called a *finite-state thread*.

We extend BTA with guarded recursion by adding constants for solutions of guarded recursive specifications and axioms concerning these additional constants. For each guarded recursive specification E and each $X \in V(E)$, we add a constant of sort \mathbf{T} standing for the unique solution of E for X to the constants of BTA. The constant standing for the unique solution of E for X is denoted by $\langle X|E \rangle$. Moreover, we add the axioms for guarded recursion given in Table 2 to BTA, where we write $\langle t_X|E \rangle$ for t_X with, for all $Y \in V(E)$, all occurrences of Y in t_X replaced by $\langle Y|E \rangle$. In this table, X , t_X and E stand for an arbitrary variable of sort \mathbf{T} , an arbitrary BTA term of sort \mathbf{T} and an arbitrary guarded recursive specification over BTA, respectively. Side conditions are added to restrict the variables, terms and guarded recursive specifications for which X , t_X and E stand. \mathbf{RDP} stands for recursive definition principle and \mathbf{RSP} stands for recursive specification principle. The equations $\langle X|E \rangle = \langle t_X|E \rangle$ for a fixed E express that the constants $\langle X|E \rangle$ make up a solution of E . The conditional equations $E \Rightarrow X = \langle X|E \rangle$ express that this solution is the only one. It is easily demonstrated that \mathbf{RDP} and \mathbf{RSP} hold in the model of BTA based on projective sequences outlined in [3] (cf. Theorem 1 from [8]).

We will use the following abbreviation: a^ω , where $a \in \mathcal{A}_{\text{tau}}$, abbreviates $\langle X|\{X = a \circ X\} \rangle$.

We will write $\mathbf{BTA+REC}$ for BTA extended with the constants for solutions of guarded recursive specifications and axioms \mathbf{RDP} and \mathbf{RSP} .

In [4], we show that the threads considered in $\mathbf{BTA+REC}$ can be viewed as processes that are definable over \mathbf{ACP} [12].

3. Program Algebra

In this section, we review PGA (ProGram Algebra), an algebra of sequential programs based on the idea that sequential programs are in essence sequences of instructions. PGA provides a program notation for finite-state threads.

In PGA, it is assumed that there is a fixed but arbitrary finite set \mathfrak{A} of *basic instructions*. PGA has the following *primitive instructions*:

- for each $a \in \mathfrak{A}$, a *plain basic instruction* a ;
- for each $a \in \mathfrak{A}$, a *positive test instruction* $+a$;
- for each $a \in \mathfrak{A}$, a *negative test instruction* $-a$;
- for each $l \in \mathbb{N}$, a *forward jump instruction* $\#l$;
- a *termination instruction* $!$.

We write \mathfrak{I} for the set of all primitive instructions.

The intuition is that the execution of a basic instruction a may modify a state and produces T or F at its completion. In the case of a positive test instruction $+a$, basic instruction a is executed and execution proceeds with the next primitive instruction if T is produced; otherwise, the next primitive instruction is skipped and execution proceeds with the primitive instruction following the skipped one. In the case where T is produced and there is not at least one subsequent primitive instruction and in the case where F is produced and there are not at least two subsequent primitive instructions, inaction occurs. In the case of a negative test instruction $-a$, the role of the value produced is reversed. In the case of a plain basic instruction a , the value produced is disregarded: execution always proceeds as if T is produced. The effect of a forward jump instruction $\#l$ is that execution proceeds with the l -th next instruction of the program concerned. If l equals 0 or the l -th next instruction does not exist, then $\#l$ results in inaction. The effect of the termination instruction $!$ is that execution terminates.

PGA has the following constants and operators:

- for each $u \in \mathfrak{I}$, an *instruction constant* u ;
- the binary *concatenation operator* $_ ; _$;
- the unary *repetition operator* $_^\omega$.

Terms are built as usual. Throughout the paper, we assume that there are infinitely many variables, including x, y, z .

We use infix notation for concatenation and postfix notation for repetition.

Closed PGA terms are considered to denote programs. The intuition is that a program is in essence a non-empty, finite or infinite sequence of primitive instructions. These sequences are called *single pass instruction sequences* because PGA has been designed to enable single pass execution of instruction sequences: each instruction can be dropped after it has been executed. Programs are considered to be equal if they represent the same single pass instruction sequence. The axioms for instruction sequence equivalence are given in Table 3. In this table, n stands for an arbitrary natural number greater than 0. For each $n > 0$, the term x^n is defined by induction on n as follows: $x^1 = x$ and $x^{n+1} = x ; x^n$.

Table 3. Axioms of PGA

$(x ; y) ; z = x ; (y ; z)$	PGA1
$(x^n)^\omega = x^\omega$	PGA2
$x^\omega ; y = x^\omega$	PGA3
$(x ; y)^\omega = x ; (y ; x)^\omega$	PGA4

Table 4. Defining equations for thread extraction operation

$ a = a \circ D$	$ \#l = D$
$ a ; x = a \circ x $	$ \#0 ; x = D$
$ +a = a \circ D$	$ \#1 ; x = x $
$ +a ; x = x \trianglelefteq a \triangleright \#2 ; x $	$ \#l + 2 ; u = D$
$ -a = a \circ D$	$ \#l + 2 ; u ; x = \#l + 1 ; x $
$ -a ; x = \#2 ; x \trianglelefteq a \triangleright x $	$ \! = S$
	$ \! ; x = S$

Table 5. Rule for infinite jump chains

$$\frac{x \cong \#0 ; y \Rightarrow |x| = D}{x \cong \#0 ; y \Rightarrow |x| = D}$$

Table 6. Defining formulas for structural congruence predicate

$\#n + 1 ; u_1 ; \dots ; u_n ; \#0 \cong \#0 ; u_1 ; \dots ; u_n ; \#0$
$\#n + 1 ; u_1 ; \dots ; u_n ; \#m \cong \#m + n + 1 ; u_1 ; \dots ; u_n ; \#m$
$(\#n + l + 1 ; u_1 ; \dots ; u_n)^\omega \cong (\#l ; u_1 ; \dots ; u_n)^\omega$
$\#m + n + l + 2 ; u_1 ; \dots ; u_n ; (v_1 ; \dots ; v_{m+1})^\omega \cong$ $\#n + l + 1 ; u_1 ; \dots ; u_n ; (v_1 ; \dots ; v_{m+1})^\omega$
$x \cong x$
$x_1 \cong y_1 \wedge x_2 \cong y_2 \Rightarrow x_1 ; x_2 \cong y_1 ; y_2 \wedge x_1^\omega \cong y_1^\omega$

The *unfolding* equation $x^\omega = x ; x^\omega$ is derivable. Each closed PGA term is derivably equal to a term in *canonical form*, i.e. a term of the form P or $P ; Q^\omega$, where P and Q are closed PGA terms that do not contain the repetition operator.

Each closed PGA term is considered to denote a program of which the behaviour is a finite-state thread, taking the set \mathfrak{A} of basic instructions for the set \mathcal{A} of actions. The *thread extraction* operation $|-|$ assigns a thread to each program. The thread extraction operation is defined by the equations given in Table 4 (for $a \in \mathfrak{A}$, $l \in \mathbb{N}$ and $u \in \mathfrak{I}$) and the rule given in Table 5. This rule is expressed in terms of the *structural congruence* predicate \cong , which is defined by the formulas given in Table 6 (for $n, m, l \in \mathbb{N}$ and $u_1, \dots, u_n, v_1, \dots, v_{m+1} \in \mathfrak{I}$).

The equations given in Table 4 do not cover the case where there is an infinite chain of forward jumps. Programs are structural congruent if they are the same after removing all chains of forward jumps in favour of single jumps. Because an infinite chain of forward jumps corresponds to $\#0$, the rule from Table 5 can be read as follows: if x starts with an infinite chain of forward jumps, then $|x|$ equals D . It is easy to see that the thread extraction operation assigns the same thread to structurally congruent programs. Therefore, the rule from Table 5 can be replaced by the following generalization: $x \cong y \Rightarrow |x| = |y|$.

Let E be a finite guarded recursive specification over BTA, and let P_X be a closed PGA term for each $X \in V(E)$. Let E' be the set of equations that results from replacing in E all occurrences of X by $|P_X|$ for each $X \in V(E)$. If E' can be obtained by applications of axioms PGA1–PGA4, the defining equations for the thread extraction operation and the rule for infinite jump chains, then $|P_X|$ is the solution of E for X . Such a finite guarded recursive specification can always be found. Thus, the behaviour of each closed PGA term is a thread that is definable by a finite guarded recursive specification over BTA. Moreover, each finite guarded recursive specification over BTA can be translated to a closed PGA term of which the behaviour is the solution of the finite guarded recursive specification concerned (see Proposition 2 of [15]).

Closed PGA terms are loosely called *PGA programs*. PGA programs in which the repetition operator does not occur are called *finite* PGA programs.

4. The Program Notation PGLD

In this section, we review a program notation which is rooted in PGA. This program notation, called PGLD, belongs to a hierarchy of program notations introduced in [3]. PGLD is close to existing assembly languages. It has absolute jump instructions and no explicit termination instruction.

In PGLD, as in PGA, it is assumed that there is a fixed but arbitrary finite set of *basic instructions* \mathfrak{A} . Again, the intuition is that the execution of a basic instruction a may modify a state and produces T or F at its completion.

PGLD has the following primitive instructions:

- for each $a \in \mathfrak{A}$, a *plain basic instruction* a ;
- for each $a \in \mathfrak{A}$, a *positive test instruction* $+a$;
- for each $a \in \mathfrak{A}$, a *negative test instruction* $-a$;
- for each $l \in \mathbb{N}$, a *direct absolute jump instruction* $\#\#l$.

PGLD programs have the form $u_1; \dots; u_k$, where u_1, \dots, u_k are primitive instructions of PGLD. We write $\mathcal{P}_{\text{PGLD}}$ for the set of all PGLD programs.

The plain basic instructions, the positive test instructions, and the negative test instructions are as in PGA. The effect of a direct absolute jump instruction $\#\#l$ is that execution proceeds with the l -th instruction of the program concerned. If $\#\#l$ is itself the l -th instruction, then inaction occurs. If l equals 0 or l is greater than the length of the program, then termination occurs.

We define the meaning of PGLD programs by means of a function pgld2pga from the set of all PGLD programs to the set of all PGA programs. This function is defined by

$$\text{pgld2pga}(u_1; \dots; u_k) = (\phi_1(u_1); \dots; \phi_k(u_k); !; !)^{\omega},$$

where the auxiliary functions ϕ_j from the set of all primitive instructions of PGLD to the set of all primitive instructions of PGA are defined as follows ($1 \leq j \leq k$):

$$\begin{aligned} \phi_j(\#\#l) &= \#l - j && \text{if } j \leq l \leq k, \\ \phi_j(\#\#l) &= \#k + 2 - (j - l) && \text{if } 0 < l < j, \\ \phi_j(\#\#l) &= ! && \text{if } l = 0 \vee l > k, \\ \phi_j(u) &= u && \text{if } u \text{ is not a jump instruction.} \end{aligned}$$

Let P be a PGLD program. Then $\text{pgld2pga}(P)$ represents the meaning of P as a PGA program. The intended behaviour of P under execution is the behaviour of $\text{pgld2pga}(P)$ under execution. That is, the *behaviour* of P under execution, written $|P|_{\text{PGLD}}$, is $|\text{pgld2pga}(P)|$.

We use the phrase *projection semantics* to refer to the approach to semantics followed in this section. The meaning function pgld2pga is called a *projection*.

In the hierarchy of program notations introduced in [3], program notations PGLA, PGLB and PGLC appear between PGA and PGLD. In [3], PGLD programs are translated into PGLC programs by means of a projection pgld2pglc , etc. Above, pgld2pga is defined such that $\text{pgld2pga}(P) = \text{pgla2pga}(\text{pglb2pgla}(\text{pglc2pglb}(\text{pgld2pglc}(P))))$ for all PGLD programs P .

5. A Classification of Services

In this short section, we provide a classification of services. It is a simplified version of the classification given in [9] and will be used in subsequent sections.

A distinction is made between target services and para-target services:

- A service is a *target service* if the result of the processing of commands by the service is partly observable externally. Reading input data from a keyboard, showing output data on a screen and writing persistent data in permanent memory are typical examples of using a target service.
- A service is a *para-target service* if the result of the processing of commands by the service is wholly unobservable externally. Setting a timer and transferring data by means of a Java pipe are typical examples of using a para-target service.

The overall intuition about threads, target services and para-target services is that:

- a thread is the behaviour exhibited by a sequential program on execution;
- a thread interacts with services provided by the execution environment in question;
- the intentions about the resulting behaviour pertain only to interaction with target services;
- interaction with para-target services takes place only in as far as it is needed to obtain the intended behaviour in relation to target services.

One of the assumptions made in thread algebra is that para-target services are deterministic. The exclusion of non-deterministic para-target services is a simplification. We believe however that this simplification is adequate in the cases that we address: para-target services that keep data for a thread. Of

course, it is inadequate in cases where services such as dice-playing services are taken into consideration. Another assumption is that target services are non-deterministic. The reason for this assumption is that the dependence of target services on external conditions make it appear to threads that they behave non-deterministically.

6. An Action Transforming Use Mechanism

A thread may perform certain basic actions only for the sake of having itself affected by a service. When processing a basic action performed by a thread, a service affects that thread in one of the following ways: (i) by returning a reply value to the thread at completion of the processing of the basic action; (ii) by turning the processed basic action into another basic action. In this section, we introduce an action transforming use mechanism, which allows for para-target services to affect threads in either way. We will only use the action transforming use mechanism to have program behaviours affected by a para-target service. The action transforming use mechanism is a generalization of the version of the use mechanism introduced in [7].²

It is assumed that there is a fixed but arbitrary finite set of *foci* \mathcal{F} and a fixed but arbitrary finite set of *methods* \mathcal{M} . Each focus plays the role of a name of a service provided by the execution environment that can be requested to process a command. Each method plays the role of a command proper. For the set \mathcal{A} of basic actions, we take the set $\{f.m \mid f \in \mathcal{F}, m \in \mathcal{M}\}$. Performing a basic action $f.m$ is taken as making a request to the service named f to process command m .

We introduce yet another sort: the sort \mathbf{S} of *services*. However, we will not introduce constants and operators to build terms of this sort. We identify para-target services with pairs (H_1, H_2) , where $H_1 : \mathcal{M}^+ \rightarrow \{\mathbf{T}, \mathbf{F}, \mathbf{M}, \mathbf{B}\}$ and $H_2 : \mathcal{M}^+ \rightarrow \mathcal{A}_{\text{tau}}$, satisfying the following conditions:³

$$\forall m \in \mathcal{M} \cdot (\exists \alpha \in \mathcal{M}^* \cdot H_1(\alpha \circ \langle m \rangle) = \mathbf{M} \Rightarrow \forall \alpha' \in \mathcal{M}^* \cdot H_1(\alpha' \circ \langle m \rangle) \notin \{\mathbf{T}, \mathbf{F}\}) ,$$

$$\forall \alpha \in \mathcal{M}^+, m \in \mathcal{M} \cdot (H_1(\alpha) = \mathbf{B} \Rightarrow H_1(\alpha \circ \langle m \rangle) = \mathbf{B}) ,$$

$$\forall \alpha \in \mathcal{M}^+ \cdot (H_1(\alpha) \neq \mathbf{M} \Leftrightarrow H_2(\alpha) = \text{tau}) .$$

\mathbf{M} stands for meaningless and \mathbf{B} stands for blocked. \mathbf{M} is used to indicate that a request to process a command is accepted, but that no reply is produced. \mathbf{B} is used to indicate that a request to process a command is rejected.

Let H be a para-target service, and let H_1 and H_2 be the unique functions such that $H = (H_1, H_2)$. Then we write $rf(H)$ and $af(H)$ for H_1 and H_2 , respectively. Given a para-target service H and a method $m \in \mathcal{M}$, the *derived service* of H after processing m , written $\frac{\partial}{\partial m}H$, is defined by $rf(\frac{\partial}{\partial m}H)(\alpha) = rf(H)(\langle m \rangle \circ \alpha)$ and $af(\frac{\partial}{\partial m}H)(\alpha) = af(H)(\langle m \rangle \circ \alpha)$.

A para-target service H can be understood as follows:

- if $rf(H)(\langle m \rangle) \in \{\mathbf{T}, \mathbf{F}\}$, then the request to process m is accepted by the service, the reply $rf(H)(\langle m \rangle)$ is produced, m is turned into tau, and the service proceeds as $\frac{\partial}{\partial m}H$;

²In later papers, this use mechanism is also called thread-service composition.

³We write D^* for the set of all finite sequences with elements from set D and we write D^+ for the set of all non-empty finite sequences with elements from set D . We use the following notation for finite sequences: $\langle \rangle$ for the empty sequence, $\langle d \rangle$ for the sequence having d as sole element, and $\sigma \circ \sigma'$ for the concatenation of finite sequences σ and σ' .

Table 7. Axioms for action transforming use operators

$S /_f H = S$	ATU1
$D /_f H = D$	ATU2
$(\text{tau} \circ x) /_f H = \text{tau} \circ (x /_f H)$	ATU3
$(x \trianglelefteq g.m \trianglerighteq y) /_f H = (x /_f H) \trianglelefteq g.m \trianglerighteq (y /_f H)$ if $f \neq g$	ATU4
$(x \trianglelefteq f.m \trianglerighteq y) /_f H = \text{tau} \circ (x /_f \frac{\partial}{\partial m} H)$	if $rf(H)(\langle m \rangle) = \text{T}$ ATU5
$(x \trianglelefteq f.m \trianglerighteq y) /_f H = \text{tau} \circ (y /_f \frac{\partial}{\partial m} H)$	if $rf(H)(\langle m \rangle) = \text{F}$ ATU6
$(x \trianglelefteq f.m \trianglerighteq y) /_f H =$ $(x /_f \frac{\partial}{\partial m} H) \trianglelefteq af(H)(\langle m \rangle) \trianglerighteq (y /_f \frac{\partial}{\partial m} H)$	if $rf(H)(\langle m \rangle) = \text{M}$ ATU7
$(x \trianglelefteq f.m \trianglerighteq y) /_f H = D$	if $rf(H)(\langle m \rangle) = \text{B}$ ATU8

- if $rf(H)(\langle m \rangle) = \text{M}$, then the request to process m is accepted by the service, no reply is produced, m is turned into $af(H)(\langle m \rangle)$, and the service proceeds as $\frac{\partial}{\partial m} H$;
- if $rf(H)(\langle m \rangle) = \text{B}$, then the request to process m is rejected by the service.

The three conditions imposed on para-target services can be paraphrased as follows:

- if it is possible that no reply is produced at completion of the processing of a command, then it is impossible that a positive or negative reply is produced at completion of the processing of that command;
- after a request to process a command has been rejected, any request to process a command will be rejected;
- a reply is produced at completion of the processing of a command if and only if the command is turned into tau.

For each $f \in \mathcal{F}$, we introduce the binary *action transforming use operator* $- /_f - : \mathbf{T} \times \mathbf{S} \rightarrow \mathbf{T}$. Intuitively, $p /_f H$ is the thread that results from processing all basic actions performed by thread p that are of the form $f.m$ by the para-target service H . When a basic action of the form $f.m$ performed by thread p is processed by the para-target service H , it is turned into another action and, if this action is tau, postconditional composition is removed in favour of action prefixing on the basis of the reply value produced.

The axioms for the action transforming use operators are given in Table 7. In this table, f and g stand for arbitrary foci from \mathcal{F} and m stands for an arbitrary method from \mathcal{M} . Axioms ATU3 and ATU4 express that the action tau and basic actions of the form $g.m$, where $f \neq g$, are not processed. Axioms ATU5–ATU7 express that a thread is affected by a para-target service as described above when a basic action of the form $f.m$ performed by the thread is processed by the service. Axiom ATU8 expresses that inaction occurs when a basic action to be processed is not accepted.

Let T stand for either BTA or BTA+REC. Then we will write $T+\text{ATU}$ for T , taking the set $\{f.m \mid f \in \mathcal{F}, m \in \mathcal{M}\}$ for \mathcal{A} , extended with the action transforming use operators and the axioms from Table 7.

Table 8. Axioms for abstraction

$\tau_{\mathbf{tau}}(\mathbf{S}) = \mathbf{S}$	TT1
$\tau_{\mathbf{tau}}(\mathbf{D}) = \mathbf{D}$	TT2
$\tau_{\mathbf{tau}}(\mathbf{tau} \circ x) = \tau_{\mathbf{tau}}(x)$	TT3
$\tau_{\mathbf{tau}}(x \trianglelefteq a \triangleright y) = \tau_{\mathbf{tau}}(x) \trianglelefteq a \triangleright \tau_{\mathbf{tau}}(y)$	TT4
$\tau_{\mathbf{tau}}(\mathbf{tau}^\omega) = \mathbf{D}$	TT5

The use mechanism introduced in [7] deals in essence with para-target services H for which it holds that $af(H)(\alpha) = \mathbf{tau}$ for all $\alpha \in \mathcal{M}^+$. For these services, the action transforming use mechanism coincides with the use mechanism from [7].

The action \mathbf{tau} is an internal action whose presence matters. To conceal its presence in the case where it does not matter after all, we also introduce the unary *abstraction* operator $\tau_{\mathbf{tau}} : \mathbf{T} \rightarrow \mathbf{T}$.

The axioms for the abstraction operator are given in Table 8. In this table, a stands for an arbitrary basic action from \mathcal{A} .

Abstraction can for instance be appropriate in cases where \mathbf{tau} arises from turning actions of an auxiliary nature into \mathbf{tau} by means of the action transforming use mechanism. In subsequent sections, abstraction will only be used in such cases. Unlike the use mechanism introduced in [7], the use mechanism introduced in [11] incorporates abstraction.

Let T stand for either BTA+REC or BTA+REC+ATU. Then we will write T +ABSTR for T extended with the abstraction operator and the axioms from Table 8.

7. State-Based Description of Para-Target Services

In this section, we introduce the state-based approach to describe families of para-target services that will be used in Sections 8 and 11. This approach is similar to the approach to describe state machines introduced in [11].

In this approach, a family of para-target services is described by

- a set of states S ;
- an effect function $eff : \mathcal{M} \times S \rightarrow S$;
- a yield function $yld : \mathcal{M} \times S \rightarrow \{\mathbf{T}, \mathbf{F}, \mathbf{M}, \mathbf{B}\}$;
- an action function $act : \mathcal{M} \times S \rightarrow \mathcal{A}_{\mathbf{tau}}$;

satisfying the following conditions:

$$\forall m \in \mathcal{M} \cdot (\exists s \in S \cdot yld(m, s) = \mathbf{M} \Rightarrow \forall s' \in S \cdot yld(m, s') \notin \{\mathbf{T}, \mathbf{F}\}) ,$$

$$\exists s \in S \cdot \forall m \in \mathcal{M} \cdot (yld(m, s) = \mathbf{B} \wedge \forall s' \in S \cdot (yld(m, s') = \mathbf{B} \Rightarrow eff(m, s') = s)) ,$$

$$\forall m \in \mathcal{M}, s \in S \cdot (yld(m, s) \neq \mathbf{M} \Leftrightarrow act(m, s) = \mathbf{tau}) .$$

The set S contains the states in which the services may be, and the functions eff , yld and act give, for each method m and state s , the state, reply and action, respectively, that result from processing m in state s .

We define, for each $s \in S$, a cumulative effect function $ceff_s : \mathcal{M}^* \rightarrow S$ in terms of s and eff as follows:

$$\begin{aligned} ceff_s(\langle \rangle) &= s, \\ ceff_s(\alpha \curvearrowright \langle m \rangle) &= eff(m, ceff_s(\alpha)). \end{aligned}$$

We define, for each $s \in S$, a para-target service H_s in terms of $ceff_s$, yld and act as follows:

$$\begin{aligned} rf(H_s)(\alpha \curvearrowright \langle m \rangle) &= yld(m, ceff_s(\alpha)), \\ af(H_s)(\alpha \curvearrowright \langle m \rangle) &= act(m, ceff_s(\alpha)). \end{aligned}$$

H_s is called the para-target service with *initial state* s described by S , eff , yld and act . We say that $\{H_s \mid s \in S\}$ is the *family of para-target services* described by S , eff , yld and act .

The conditions that are imposed on S , eff , yld and act imply that, for each $s \in S$, H_s is indeed a para-target service. It is worth mentioning that $\frac{\partial}{\partial m} H_s = H_{eff(m,s)}$, $rf(H_s)(\langle m \rangle) = yld(m, s)$, and $af(H_s)(\langle m \rangle) = act(m, s)$.

8. Method-to-Action Translator Services

In this section, we give a state-based description of the very simple family of para-target services that constitute a register-file-dependent method-to-action translator of which the register file consists of registers that can contain natural numbers up to some bound. This method-to-action translator will be used in Section 9 to describe the behaviour of programs in a variant of PGLD with dynamically instantiated instructions.

It is assumed that fixed but arbitrary positive numbers $I, N \in \mathbb{N}$ have been given. I is considered the number of registers in the register file and N is considered the greatest natural number that can be contained in the registers of the register file. The functions from $[1, I]$ to $[0, N]$ are taken for the states of the register file. For every function $s : [1, I] \rightarrow [0, N]$, s is the state in which, for each $i \in [1, I]$, the content of register i is $s(i)$.

It is also assumed that a fixed but arbitrary set $\mathcal{A}_{\text{proto}} \subseteq \mathcal{M}$ and a fixed but arbitrary function $\theta : \mathcal{A}_{\text{proto}} \times ([1, I] \rightarrow [0, N]) \rightarrow \mathcal{A}$ have been given. $\mathcal{A}_{\text{proto}}$ is considered the set of methods that are transformable to basic actions and θ is regarded to give, for each method m in $\mathcal{A}_{\text{proto}}$ and function $s : [1, I] \rightarrow [0, N]$, the basic action into which m is turned in the case where the state of the register file is s . The methods that belong to $\mathcal{A}_{\text{proto}}$ are called *proto-actions* because they are the methods that are turned into basic actions by the register-file-dependent method-to-action translator.

The register-file-dependent method-to-action translator services accept the following methods:

- for each $i \in [1, I]$ and $n \in [0, N]$, a *register set method* $\text{set}:i:n$;
- each $m \in \mathcal{A}_{\text{proto}}$.

We write \mathcal{M}_{set} for the set $\{\text{set}:i:n \mid i \in [1, I] \wedge n \in [0, N]\}$. It is assumed that $\mathcal{M}_{\text{set}} \subseteq \mathcal{M}$.

The methods accepted by the method-to-action translator services can be explained as follows:

- $\text{set}:i:n$: the content of register i becomes n , the reply is \top , and $\text{set}:i:n$ is turned into tau;
- m , where $m \in \mathcal{A}_{\text{proto}}$: the state of the register file does not change, there is no reply, and m is turned into $\theta(m, s)$ where s is the state of the register file.

Let RFS be the set of all functions $s: [1, I] \rightarrow [0, N]$. Take \uparrow such that $\uparrow \notin RFS$. Let $s \in RFS \cup \{\uparrow\}$. Then we write $RFDT_s$ for the para-target service with initial state s described by $S = RFS \cup \{\uparrow\}$ and the functions eff , yld , and act defined as follows ($i \in [1, I]$, $n \in [0, N]$, and $\rho \in RFS$):⁴

$$\begin{aligned} \text{eff}(\text{set}:i:n, \rho) &= \rho \oplus [i \mapsto n],^5 & \text{yld}(\text{set}:i:n, \rho) &= \top, \\ \text{eff}(m, \rho) &= \rho & \text{if } m \in \mathcal{A}_{\text{proto}}, & \text{yld}(m, \rho) = \text{M} & \text{if } m \in \mathcal{A}_{\text{proto}}, \\ \text{eff}(m, \rho) &= \uparrow & \text{if } m \notin \mathcal{M}_{\text{set}} \cup \mathcal{A}_{\text{proto}}, & \text{yld}(m, \rho) = \text{B} & \text{if } m \notin \mathcal{M}_{\text{set}} \cup \mathcal{A}_{\text{proto}}, \\ \text{eff}(m, \uparrow) &= \uparrow, & & \text{yld}(m, \uparrow) = \text{B}, \\ \\ \text{act}(m, \rho) &= \theta(m, \rho) & \text{if } m \in \mathcal{A}_{\text{proto}}, & \\ \text{act}(m, \rho) &= \text{tau} & \text{if } m \notin \mathcal{A}_{\text{proto}}, & \\ \text{act}(m, \uparrow) &= \text{tau}. & & \end{aligned}$$

We write $RFDT_{\text{init}}$ for $RFDT_{[1 \mapsto 0] \oplus \dots \oplus [I \mapsto 0]}$.

The special state \uparrow added above to the proper states of the register file is a state in which any request to process a method is rejected. The existence of such a blocking state is required to guarantee that S , eff , yld and act describe a para-target service.

The following proposition states rigorously that the methods that belong to $\mathcal{A}_{\text{proto}}$ are exactly the methods that are turned into basic actions.

Proposition 8.1. For all $s: [1, I] \rightarrow [0, N]$:

$$\mathcal{A}_{\text{proto}} = \{m \in \mathcal{M} \mid \exists \alpha \in \mathcal{M}^* \cdot \text{af}(RFDT_s)(\alpha \curvearrowright \langle m \rangle) \in \mathcal{A}\}.$$

Proof:

This follows immediately from the definition of the register-file-dependent method-to-action translator services. \square

9. PGLD with Dynamically Instantiated Instructions

In this section, we introduce a variant of PGLD with dynamically instantiated instructions. This variant is called PGLD_{dii} . In Section 10, the usefulness of dynamic instruction instantiation will be illustrated by means of an example.

In PGLD_{dii} , it is assumed that there is a fixed but arbitrary finite set of *foci* \mathcal{F} with $\text{rfdt} \in \mathcal{F}$ and a fixed but arbitrary finite set of *methods* \mathcal{M} . Moreover, we adopt the assumptions made about register-file-

⁴We use the following notation for functions: $f \oplus g$ for the function h with $\text{dom}(h) = \text{dom}(f) \cup \text{dom}(g)$ such that for all $d \in \text{dom}(h)$, $h(d) = f(d)$ if $d \notin \text{dom}(g)$ and $h(d) = g(d)$ otherwise; and $[d \mapsto r]$ for the function f with $\text{dom}(f) = \{d\}$ such that $f(d) = r$.

dependent method-to-action translator services in Section 8. The set $\{f.m \mid f \in \mathcal{F}, m \in \mathcal{M} \setminus \mathcal{A}_{\text{proto}}\}$ is taken as the set \mathfrak{A} of basic instructions. In the setting of PGLD_{dii} , we use the term *proto-instruction* instead of proto-action and write $\mathfrak{A}_{\text{proto}}$ instead of $\mathcal{A}_{\text{proto}}$. A proto-instruction is what becomes a basic instruction by dynamic instantiation.

PGLD_{dii} has the following primitive instructions in addition to the primitive instructions of PGLD:

- for each $e \in \mathfrak{A}_{\text{proto}}$, a *plain basic proto-instruction* e ;
- for each $e \in \mathfrak{A}_{\text{proto}}$, a *positive test proto-instruction* $+e$;
- for each $e \in \mathfrak{A}_{\text{proto}}$, a *negative test proto-instruction* $-e$.

PGLD_{dii} programs have the form $u_1 ; \dots ; u_k$, where u_1, \dots, u_k are primitive instructions of PGLD_{dii} .

The effect of a plain basic proto-instruction e is the same as the effect of the plain basic instruction $\theta(e, s)$, where s is the state of the register file involved in the instantiation of proto-instructions. The effect of a positive or negative test proto-instruction is similar.

Recall that the content of register i can be set to n by means of the basic instruction $\text{rfdt.set}:i:n$. Initially, its content is 0.

We define the meaning of PGLD_{dii} programs by means of a function pglddii2pgld from the set of all PGLD_{dii} programs to the set of all PGLD programs. This function is defined by

$$\text{pglddii2pgld}(u_1 ; \dots ; u_k) = \psi(u_1) ; \dots ; \psi(u_k) ,$$

where the auxiliary function ψ from the set of all primitive instructions of PGLD_{dii} to the set of all primitive instructions of PGLD is defined as follows:

$$\begin{aligned} \psi(e) &= \text{rfdt}.e && \text{if } e \in \mathfrak{A}_{\text{proto}} , \\ \psi(+e) &= +\text{rfdt}.e && \text{if } e \in \mathfrak{A}_{\text{proto}} , \\ \psi(-e) &= -\text{rfdt}.e && \text{if } e \in \mathfrak{A}_{\text{proto}} , \\ \psi(u) &= u && \text{if } u \text{ is not a proto-instruction .} \end{aligned}$$

The idea is that each proto-instruction can be replaced by an instruction in which the proto-instruction is taken for the method.

Let P be a PGLD_{dii} program. Then $\text{pglddii2pgld}(P)$ represents the meaning of P as a PGLD program. The intended behaviour of P under execution is the behaviour of $\text{pglddii2pgld}(P)$ under execution on interaction with a register-file-dependent method-to-action translator when abstracted from τ . That is, the *behaviour* of P under execution, written $|P|_{\text{PGLD}_{\text{dii}}}$, is $\tau_{\tau}(|\text{pglddii2pgld}(P)|_{\text{PGLD}} / \text{rfdt} \text{RFDT}_{\text{init}})$.

10. Concrete Proto-Instructions

At a fairly concrete level, basic instructions and proto-instructions are strings of characters. In [3], a concrete notation for basic instructions is introduced for the case where each basic instruction consists of a focus and a method. Here, we extend that concrete notation to cover proto-instructions. The resulting concrete notation will be used in examples of the use of PGLD_{dii} .

First of all, we distinguish neutral strings and active strings. A *neutral string* is an empty string or a string of one or more characters of which the first character is a letter or a colon and each of the remaining characters is a letter, a digit or a colon. An *active string* is a string of two or more characters of which the first character is an asterisk and each of the remaining characters is a digit.

A *concrete proto-instruction* is a string of the form $f'.m'$, where f' and m' are non-empty strings of characters in which neutral strings and active strings alternate, starting with a neutral string of which the first character is a letter, and at least one active string occurs.

A *concrete focus* is a neutral string of which the first character is a letter. A *concrete method* is either a neutral string of which the first character is a letter or a concrete proto-instruction. A *concrete instruction* is a string of the form $f.m$, where f is a concrete focus and m is a concrete method.

The intention is that instantiation of a concrete proto-instruction amounts to simultaneously replacing all active strings occurring in it by strings according to some assignment of strings to active strings. The assignment concerned must be such that concrete proto-instructions are turned into concrete instructions.

To accomplish the assignments of strings to active strings, all active strings of interest must be of the form $*\delta$, where δ is the decimal representation of some $i \in [1, I]$. Moreover, an encoding of the assignable strings by numbers in $[0, N]$ must be given. Then each state of the register file being involved in PGLD_{dii} induces an assignment as follows: for each active string of interest, say $*\delta$, the string assigned to it is the one that is encoded by the content of the register with the number of which δ is the decimal representation.

The concrete notation for basic instructions introduced above is sufficiently expressive for all applications that we have in mind. The assignable strings are in many cases binary or decimal representations of numbers in the interval $[0, N]$. In such cases, it is most natural to encode the representations simply by the numbers that they represent.

Example 10.1. Consider a program that on execution reads digit by digit the binary representation of a password and then performs an action to have the password checked by some para-target service. The binary representation of a password is a character sequence of a fixed length, say n , of which all characters are among the binary digits 0 and 1. The program reads in the binary digits which make up the binary representation of the password by performing actions that are processed by a target service. Suppose that the service used for reading in binary digits only accepts methods of the form getb and returns the reply F if the next binary digit is 0 and T if the next binary digit is 1. Moreover, suppose that the service used for checking passwords only accepts methods of the form chk:pw , where pw is the binary representation of a password. The focus stdin is used below as a name of the former service and the focus passw is used below as a name of the latter service.

In PGLD_{dii} , where proto-instructions are available, the program has to distinguish among only $2 \cdot n$ cases. In PGLD , where no proto-instructions are available, the program has to distinguish among 2^n cases.

Take $I = n$ and $N = 1$. Consider the case where $n = 3$. In PGLD_{dii} , the initial part of the program looks as follows:

```
+stdin.getb ; ##5 ; rfdt.set:1:0 ; ##6 ; rfdt.set:1:1 ;
+stdin.getb ; ##10 ; rfdt.set:2:0 ; ##11 ; rfdt.set:2:1 ;
+stdin.getb ; ##15 ; rfdt.set:3:0 ; ##16 ; rfdt.set:3:1 ;
+passw.chk:*1:*2:*3 ; ...
```

In PGLD, the initial part of the program looks as follows:

```
+stdin.getb ; ##7 ; ##4 ;
+stdin.getb ; ##13 ; ##10 ; +stdin.getb ; ##19 ; ##16 ;
+stdin.getb ; ##23 ; ##22 ; +stdin.getb ; ##25 ; ##24 ;
+stdin.getb ; ##27 ; ##26 ; +stdin.getb ; ##29 ; ##28 ;
+passw.chk:000 ; ##44 ; ##45 ; +passw.chk:001 ; ##44 ; ##45 ;
+passw.chk:010 ; ##44 ; ##45 ; +passw.chk:011 ; ##44 ; ##45 ;
+passw.chk:100 ; ##44 ; ##45 ; +passw.chk:101 ; ##44 ; ##45 ;
+passw.chk:110 ; ##44 ; ##45 ; +passw.chk:111 ; ...
```

These programs take 16 and 43 instructions, respectively, up to and including the password-check (proto-)instructions. In general, we have that:

- In PGLD_{dii}, the program takes $5 \cdot n + 1$ instructions up to and including the password-check proto-instruction;
- In PGLD, the program takes $6 \cdot (2^n - 1) + 1$ instructions up to and including the last password-check instruction.

11. Register File Services

In this section, we give a state-based description of the very simple family of para-target services that constitute a register file consisting of registers that can contain natural numbers up to some bound. This register file will be used in Section 12 to describe the behaviour of programs in PGLD_{dii}.

As in Section 8, it is assumed that fixed but arbitrary positive numbers $I, N \in \mathbb{N}$ have been given. I is considered the number of registers in the register file and N is considered the greatest natural number that can be contained in the registers of the register file.

The register file services accept the following methods:

- for each $i \in [1, I]$ and $n \in [0, N]$, a *register set method* $\text{set}:i:n$;
- for each $i \in [1, I]$ and $n \in [0, N]$, a *register test method* $\text{eq}:i:n$.

We write \mathcal{M}_{rf} for the set $\{\text{set}:i:n, \text{eq}:i:n \mid i \in [1, I] \wedge n \in [0, N]\}$. It is assumed that $\mathcal{M}_{\text{rf}} \subseteq \mathcal{M}$.

The methods accepted by register services can be explained as follows:

- $\text{set}:i:n$: the content of register i becomes n , the reply is \top , and $\text{set}:i:n$ is turned into τ ;
- $\text{eq}:i:n$: the content of the register does not change, the reply is \top if the content of register i equals n and F otherwise, and $\text{eq}:i:n$ is turned into τ .

Let RFS be the set of all functions $s:[1, I] \rightarrow [0, N]$. Take \uparrow such that $\uparrow \notin RFS$. Let $s \in RFS \cup \{\uparrow\}$. Then we write RF_s for the para-target service with initial state s described by $S = RFS \cup \{\uparrow\}$ and the

functions eff , yld , and act defined as follows ($i \in [1, I]$, $n \in [0, N]$, and $\rho \in RFS$):

$$\begin{aligned} eff(\text{set}:i:n, \rho) &= \rho \oplus [i \mapsto n], & yld(\text{set}:i:n, \rho) &= \top, & act(m, \rho) &= \text{tau}, \\ eff(\text{eq}:i:n, \rho) &= \rho, & yld(\text{eq}:i:n, \rho) &= \top \text{ if } \rho(i) = n, & act(m, \uparrow) &= \text{tau}. \\ eff(m, \rho) &= \uparrow \text{ if } m \notin \mathcal{M}_{\text{rf}}, & yld(\text{eq}:i:n, \rho) &= \text{F} \text{ if } \rho(i) \neq n, \\ eff(m, \uparrow) &= \uparrow, & yld(m, \rho) &= \text{B} \text{ if } m \notin \mathcal{M}_{\text{rf}}, \\ & & yld(m, \uparrow) &= \text{B}, \end{aligned}$$

We write RF_{init} for $RF_{[1 \mapsto 0] \oplus \dots \oplus [I \mapsto 0]}$.

12. An Alternative Semantics for PGLD_{dii}

In this section, we discuss an alternative semantics for PGLD_{dii} .

Unlike the meaning of PGLD_{dii} programs that we defined in Section 9, we define the alternative meaning of PGLD_{dii} programs only for the case where $I = 1$. The generalization of the definition to arbitrary I is obvious, but leads to a definition that is hard to read.

The alternative meaning of PGLD_{dii} programs is given by a function $\text{pglddii2pgld}'$ from the set of all PGLD_{dii} programs to the set of all PGLD programs. For the case where $I = 1$, this function is defined by

$$\text{pglddii2pgld}'(u_1; \dots; u_k) = \psi'_1(u_1); \dots; \psi'_k(u_k),$$

where the auxiliary functions ψ'_j from the set of all primitive instructions of PGLD_{dii} to the set of all PGLD programs are defined as follows ($1 \leq j \leq k$):

$$\begin{aligned} \psi'_j(e) &= +\text{rf.eq}:1:0; \#\#l''_{j,0}; \dots; +\text{rf.eq}:1:N-1; \#\#l''_{j,N-1}; \#\#l''_{j,N}; \\ &\quad \theta(e, 0); \#\#l'_{j+1}; \#\#l'_{j+2}; \dots; \theta(e, N-1); \#\#l'_{j+1}; \#\#l'_{j+2}; \theta(e, N), \\ \psi'_j(+e) &= +\text{rf.eq}:1:0; \#\#l''_{j,0}; \dots; +\text{rf.eq}:1:N-1; \#\#l''_{j,N-1}; \#\#l''_{j,N}; \\ &\quad +\theta(e, 0); \#\#l'_{j+1}; \#\#l'_{j+2}; \dots; +\theta(e, N-1); \#\#l'_{j+1}; \#\#l'_{j+2}; +\theta(e, N), \\ \psi'_j(-e) &= +\text{rf.eq}:1:0; \#\#l''_{j,0}; \dots; +\text{rf.eq}:1:N-1; \#\#l''_{j,N-1}; \#\#l''_{j,N}; \\ &\quad -\theta(e, 0); \#\#l'_{j+1}; \#\#l'_{j+2}; \dots; -\theta(e, N-1); \#\#l'_{j+1}; \#\#l'_{j+2}; -\theta(e, N), \\ \psi'_j(\text{rfdt}.m) &= \text{rf}.m, \\ \psi'_j(+\text{rfdt}.m) &= +\text{rf}.m, \\ \psi'_j(-\text{rfdt}.m) &= -\text{rf}.m, \\ \psi'_j(\#\#l) &= \#\#l', \\ \psi'_j(u) &= u \quad \text{if } u \text{ is not a proto-instruction, jump instruction or} \\ &\quad \text{a plain basic or test instruction with focus rfdt,} \end{aligned}$$

and for each $j \in [1, k]$ and $h \in [0, N]$:

$$\begin{aligned} l'_j &= j + (5 \cdot N + 1) \cdot n_j, \\ l''_{j,h} &= l'_j + 2 \cdot N + 3 \cdot h + 1, \end{aligned}$$

and n_j is the number of proto-instructions preceding position j .

The idea is that each proto-instruction can be replaced by an instruction sequence of which the execution leads to the execution of the intended instruction after the content of the register has been found by a linear search. Because the length of the replacing instruction sequence is greater than 1, the direct absolute jump instructions are adjusted so as to compensate for the introduction of additional instructions. Obviously, the linear search for the content of the register can be replaced by a binary search.

Henceforth, we will proceed as if $\text{pglddii2pgld}'$ has been defined for arbitrary I .

Let P be a PGLD_{dii} program. Then $\text{pglddii2pgld}'(P)$ represents an alternative meaning of P as a PGLD program. The alternative behaviour of P under execution is the behaviour of $\text{pglddii2pgld}'(P)$ under execution on interaction with a register file when abstracted from τ . That is, the *alternative behaviour* of P under execution, written $|P'_{\text{PGLD}_{\text{dii}}}|$, is $\tau_{\tau}(|\text{pglddii2pgld}'(P)|_{\text{PGLD}} /_{\text{rf}} \text{RF}_{\text{init}})$.

Example 12.1. Consider the PGLD_{dii} program from Example 10.1. The initial part of the PGLD program that results from its translation by means of pglddii2pgld looks as follows:

```
+stdin.getb ; ##5 ; rfdt.set:1:0 ; ##6 ; rfdt.set:1:1 ;
+stdin.getb ; ##10 ; rfdt.set:2:0 ; ##11 ; rfdt.set:2:1 ;
+stdin.getb ; ##15 ; rfdt.set:3:0 ; ##16 ; rfdt.set:3:1 ;
+rfdt.passw.chk:*1:*2:*3 ; ...
```

The initial part of the PGLD program that results from its translation by means of $\text{pglddii2pgld}'$ looks as follows:

```
+stdin.getb ; ##5 ; rf.set:1:0 ; ##6 ; rf.set:1:1 ;
+stdin.getb ; ##10 ; rf.set:2:0 ; ##11 ; rf.set:2:1 ;
+stdin.getb ; ##15 ; rf.set:3:0 ; ##16 ; rf.set:3:1 ;
+rf.eq:1:0 ; ##19 ; ##22 ;
+rf.eq:2:0 ; ##25 ; ##28 ;
+rf.eq:2:0 ; ##31 ; ##34 ;
+rf.eq:3:0 ; ##37 ; ##40 ;
+rf.eq:3:0 ; ##43 ; ##46 ;
+rf.eq:3:0 ; ##49 ; ##52 ;
+rf.eq:3:0 ; ##55 ; ##58 ;
+passw.chk:000 ; ##59 ; ##60 ;
+passw.chk:001 ; ##59 ; ##60 ;
+passw.chk:010 ; ##59 ; ##60 ;
+passw.chk:011 ; ##59 ; ##60 ;
+passw.chk:100 ; ##59 ; ##60 ;
+passw.chk:101 ; ##59 ; ##60 ;
+passw.chk:110 ; ##59 ; ##60 ;
+passw.chk:111 ; ...
```

These PGLD programs take 16 and 58 instructions, respectively, up to and including the password-check instructions.

Let b_1, b_2 and b_3 be either 0 or 1. Suppose that the three bits read in at the beginning of the execution of these programs are b_1, b_2 and b_3 , in that order. In the case of the former program, it is easy to check that the instruction `+rfdt.passw.chk:*1:*2:*3` will be executed while the contents of registers 1, 2 and 3 are b_1, b_2 and b_3 , respectively. In the case of the latter program, it is easy to check that the instruction `+passw.chk:b1b2b3` will be executed after execution of some test and jump instructions. This strongly suggests that the programs are “behaviourally equivalent”.

The following theorem states rigorously that, for any PGLD_{dii} program, the behaviour under execution coincides with the alternative behaviour under execution.

Theorem 12.1. For all PGLD_{dii} programs P , $|P|_{\text{PGLD}_{\text{dii}}} = |P'|_{\text{PGLD}_{\text{dii}}}$.

Proof:

Strictly speaking, we prove this theorem in the algebraic theory obtained by: (i) combining PGA with BTA+REC+ATU+ABSTR, resulting in a theory with three sorts: a sort **P** of programs, a sort **T** of threads, and a sort **S** of services; (ii) extending the result by taking $|_-$ for an additional operator from sort **P** to sort **T** and taking the semantic equations and rule defining thread extraction for additional axioms. We write \mathcal{T} for the set of all closed terms of sort **T** from the language of the resulting theory.

In the proof, we make use of an auxiliary function $|_-, -| : \mathbb{N} \times \mathcal{P}_{\text{PGLD}} \rightarrow \mathcal{T}$ which gives, for each natural number i and PGLD program $u_1 ; \dots ; u_k$, a closed term of sort **T** that denotes the behaviour of $u_1 ; \dots ; u_k$ when executed from position i if $1 \leq i \leq k$ and **S** otherwise. This function is defined as follows:

$$\begin{aligned} |i, u_1 ; \dots ; u_k| &= |\phi_i(u_i) ; \dots ; \phi_k(u_k) ; ! ; ! ; (\phi_1(u_1) ; \dots ; \phi_k(u_k) ; ! ; !)^\omega| \text{ if } 1 \leq i \leq k, \\ |i, u_1 ; \dots ; u_k| &= \mathbf{S} \text{ if } \neg 1 \leq i \leq k \end{aligned}$$

(where ϕ_j is as in the definition of `pgld2pga`). It follows easily from the definition of $|_-, -|$ and the axioms of PGA that if $1 \leq i \leq k$:

$$\begin{aligned} |i, u_1 ; \dots ; u_k| &= a \circ |i+1, u_1 ; \dots ; u_k| && \text{if } u_i = a, \\ |i, u_1 ; \dots ; u_k| &= |i+1, u_1 ; \dots ; u_k| \triangleleft a \triangleright |i+2, u_1 ; \dots ; u_k| && \text{if } u_i = +a, \\ |i, u_1 ; \dots ; u_k| &= |i+2, u_1 ; \dots ; u_k| \triangleleft a \triangleright |i+1, u_1 ; \dots ; u_k| && \text{if } u_i = -a, \\ |i, u_1 ; \dots ; u_k| &= |l, u_1 ; \dots ; u_k| && \text{if } u_i = \#\#l. \end{aligned}$$

Let v_1, \dots, v_k be primitive instructions of PGLD_{dii}, let

$$\begin{aligned} T &= \{ \tau_{\text{tau}}(|i, \psi(v_1) ; \dots ; \psi(v_k)| /_{\text{rfdt}} \text{RFDT}_s) \mid i \in [1, k] \wedge s : [1, I] \rightarrow [0, N] \}, \\ T' &= \{ \tau_{\text{tau}}(|l'_i, \psi'_1(v_1) ; \dots ; \psi'_k(v_k)| /_{\text{rf}} \text{RF}_s) \mid i \in [1, k] \wedge s : [1, I] \rightarrow [0, N] \} \end{aligned}$$

(where ψ, ψ'_j, l'_i are as in the definitions of `pglddii2pgld` and `pglddii2pgld'`), and let $\beta : T \rightarrow T'$ be the bijection defined by

$$\beta(\tau_{\text{tau}}(|i, \psi(v_1) ; \dots ; \psi(v_k)| /_{\text{rfdt}} \text{RFDT}_s)) = \tau_{\text{tau}}(|l'_i, \psi'_1(v_1) ; \dots ; \psi'_k(v_k)| /_{\text{rf}} \text{RF}_s).$$

For each $p' \in T$, write $\beta^*(p')$ for p' with, for all $p \in T$, all occurrences of p in p' replaced by $\beta(p)$. Then, using the equations concerning the auxiliary function $|_, _|$ given above, it is straightforward to prove that there exists a set E consisting of one derivable equation $p = p'$ for each $p \in T$ such that, for all equations $p = p'$ in E :

- the equation $\beta(p) = \beta^*(p')$ is derivable;
- $p' \in T$ only if p' can be rewritten to a $p'' \notin T$ using the equations in E from left to right.

Because $|\psi(v_1); \dots; \psi(v_k)| = |1, \psi(v_1); \dots; \psi(v_k)|$ and $|\psi'_1(v_1); \dots; \psi'_k(v_k)| = |l'_1, \psi'_1(v_1); \dots; \psi'_k(v_k)|$, this means that $|v_1; \dots; v_k|_{\text{PGLD}_{\text{dii}}}$ and $|v_1; \dots; v_k|'_{\text{PGLD}_{\text{dii}}}$ are solutions of the same guarded recursive specification. Because guarded recursive specifications have unique solutions, it follows immediately that $|v_1; \dots; v_k|_{\text{PGLD}_{\text{dii}}} = |v_1; \dots; v_k|'_{\text{PGLD}_{\text{dii}}}$. \square

13. Discussion of Semantic Approaches

In Sections 9 and 12, the meaning of PGLD_{dii} programs is explained by means of different translations into PGLD programs. In both sections, the intended behaviour of a PGLD_{dii} program under execution is described as the behaviour of the translated program under execution on interaction with some para-target service. The translation from Section 9 is extremely simple, but the translation from Section 12 is fairly complicated. The para-target service used in Section 9 to describe the behaviour of a PGLD_{dii} program and the one used in Section 12 are equally simple. However, the former service is far more powerful: it turns a processed method into a basic action if the method corresponds to a proto-instruction. By its power, the translation can be kept simple if that service is used. Because of the simpler translation of PGLD_{dii} programs into PGLD programs and the equally simple para-target service used, the approach followed in Section 9 to define the meaning of PGLD_{dii} programs is preferable.

A manifestation of the difference in complexity of the translations of PGLD_{dii} programs from Sections 9 and 12 is that the former translation replaces each primitive instruction of PGLD_{dii} by one primitive instruction of PGLD , whereas the latter translation gives rise to a combinatorial explosion. Recall that I stands for the number of registers involved in the instantiation of proto-instructions and N stands for the greatest natural number that can be contained in those registers. The translation from Section 12 replaces each primitive instruction of PGLD_{dii} that is not a proto-instruction by one primitive instruction of PGLD as well, but replaces each proto-instruction by a sequence of

$$(2 \cdot N + 1) \cdot \sum_{i=1}^I (N + 1)^{i-1} + 3 \cdot ((N + 1)^I - 1) + 1 = (5 \cdot N + 1) \cdot (((N + 1)^I - 1)/N) + 1$$

primitive instructions of PGLD .

If a new programming feature is added to a known program notation such as PGLD and the starting point of the approach to define the meaning of the programs from the extended program notation is translation of those programs into programs from the known program notation, then we can conceive of several approaches:

- give a translation by which each program from the extended program notation is translated into a program from the known program notation that exhibits on execution the same behaviour;

- give a translation by which each program from the extended program notation is translated into a program from the known program notation that exhibits on execution the same behaviour by interaction with a given para-target service that does not turn any processed method into a basic action;
- give a translation by which each program from the extended program notation is translated into a program from the known program notation that exhibits on execution the same behaviour by interaction with a given para-target service that turns certain processed methods into basic actions.

We consider an approach earlier in this list preferable provided that the translation concerned does not become too complicated. In the case where the translation becomes too complicated with all three approaches, it is desirable to look for another starting point. This may end up in direct thread extraction, i.e. assigning a thread to each program as this was done for PGA in Section 3.

In the case of PGLD_{dii} , it is obvious that the first approach in the list given above does not work. However, it is virtually impossible to find out that the third approach is preferable to the second one without actually producing definitions of the meaning of PGLD_{dii} programs according to both approaches.

14. Conclusions

We have studied sequential programs that are instruction sequences with dynamically instantiated instructions. We have defined the meaning of the programs concerned in two different ways, which both involve a translation into programs that are instruction sequences without dynamically instantiated instructions. In one of the two ways, the translation is very simple and does not lead to increase in the length of a program or the number of steps needed by a program. That way is considered the preferred one. The preferred way made it necessary for the use mechanism that was introduced in [7] to be generalized. In [6], we demonstrate that dynamic instruction instantiation is a useful programming feature.

In this paper, we have followed the approach of projection semantics, starting from the perception of a program as an instruction sequence. This means that programs are considered at a much lower level than usual in theoretical computer science. This allows for bringing the interface between software and hardware better into the picture, which becomes increasingly important to a growing number of developments related to computer architecture. The usual approaches to define the meaning of programs, such as operational semantics, denotational semantics and axiomatic semantics, are based on the view that the details of program execution should be abstracted from as much as possible. This makes comparisons with those approaches virtually impossible.

In [10], we have modelled and analysed micro-architectures with pipelined instruction processing in the setting of program algebra, basic thread algebra, and Maurer computers [13, 14]. In that work, which we consider a preparatory step in the development of a formal approach to design new micro-architectures, dynamically instantiated instructions were not taken into account. An option for future work is to look at the effect of dynamically instantiated instructions on pipelined instruction processing.

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