High performance N-body simulation on computational grids

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Distributed Direct $N$-body Simulations on a Global Grid of GRAPEs

Based on:
D.J. Groen, S.F. Portegies Zwart, S.L.W. McMillan and J. Makino,
Distributed N-body Simulation on the Grid Using Dedicated Hardware,

In this chapter we present our direct-method $N$-body simulations on a planet-wide network of GRAPEs connected by regular internet. The experiments were performed using grid middleware and MPI across sites.

2.1 Introduction

Star clusters are often simulated by means of direct-method $N$-body simulations [2]. The Newtonian gravitational force on individual stars in such simulations is calculated by aggregating the force contributions from all other particles in the system.

To enable faster execution of these simulations, specialized solutions such as GRAvity PipEs (GRAPEs) [36], Graphics Processing Units (GPUs) [108, 49, 18] and Field-Programmable Gate Arrays (FPGAs) [76] have been successfully developed and applied. These solutions are designed or tuned specifically for optimizing force calculations, and provide dramatic speedup. For example, the GRAPE-6Af features a dedicated hardware implementation that can calculate 42 force interactions simultaneously and with increased efficiency. As a result, the GRAPE is able to perform force calculations $\sim 130$ times faster than a single PC [86]. Recently, GPUs have shown gains in speed and flexibility, and they are now used for simulating self gravitating systems at speeds comparable to GRAPE [108, 49, 18].

Parallelization of GRAPEs appears to be an efficient way to reduce the wall-clock time for individual simulations [88, 46, 50]. The gravitational $N$-body problem has calculation time complexity $O(N^2)$, whereas the communication scales only with
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For sufficiently large $N$, the force calculation time will therefore overtake the communication time. For a local cluster of GRAPEs with low-latency and high bandwidth network, break-even between calculation and communication is reached at $N \sim 10^4$ [50].

Generally, GRAPE clusters are not cheap and few institutions can afford such dedicated hardware solutions. Still, more than 500 GRAPE modules, where one module is equivalent to one GRAPE-6Af, or 4 GRAPE-6 chips, are currently in use across 37 institutions in 12 countries world-wide. An alternative to purchasing a large GRAPE-6 or GPU cluster is provided by a computational grid. In a grid, several institutions assemble in a virtual organization, within which they share resources, and the costs for purchasing and maintaining these resources [31]. Grid middleware provides a secure wide area computing environment without requiring users to register for individual clusters. In addition, grid-enabled MPI implementations, such as MPICH-G2 [70] or OpenMPI [42], provide the ability to run MPI jobs across sites in the grid, using the existing MPI standards. Applying such grid technology to clusters of GPUs is an attractive option, because there are a large number of (frequently idle) GPUs in consumer machines. By connecting these consumer machines to the grid (as was done in a similar fashion with regular CPUs for the SETI@home project [9]) and using them for parallel $N$-body simulations, we can increase the computational power of the grid in a cheap and convenient manner.

Although there is a clear benefit of using grid technology in sharing financial burden, the real challenge is to develop new applications for astronomical problems that have yet to be solved. For example, the simulation of an entire galaxy, requires at least a few PFLOP/s of computational power and the development of a hybrid simulation environment [60]. Such an environment performs several astrophysical simulations on vastly different temporal and spatial scales. For example, a hybrid simulation environment could consist of a stellar evolution simulation to track how individual stars evolve over time, a smoothed particle hydrodynamics simulation [93] to simulate stellar collisions or close encounters, and a direct-method $N$-body calculation to simulate the remaining dynamics between stars.

To facilitate these tightly-coupled multi-physics simulations on the PFLOP/s scale, it will no longer be sufficient to do high-performance computing (HPC) on a local cluster, as we require an extensive grid infrastructure consisting of several of such clusters. Although grid technology has been largely applied to facilitate high-throughput computing [5], little research has been done on investigating how the grid can be efficiently applied to solve tightly-coupled HPC problems. By using grid technology for this specific set of problems, we can potentially fulfill the computational requirements for performing petascale multi-physics simulations.

Using a grid infrastructure for HPC has another drawback, as the communication between grid sites dramatically increases network overhead compared to a local cluster. For intercontinental communication, the network latency can become as large as 0.3s, which is especially impractical for applications, such as direct-method $N$-body codes, that require communication over all processes during every iteration. Still, even for such long communication paths there will be a problem size ($N$) for which wall-clock time is
2.2 Experiment setup

Table 2.1: Specifications for the nodes in G3. The first column gives the name of the computer followed by its country of residence (NL for the Netherlands, JP for Japan and US for the United States). The subsequent columns give the type of processor in the node, the amount of RAM, followed by the operating system and the kernel version. The machines in Japan used Globus 3.2.1, the machines in the Netherlands used Globus 4.0.3 and the machine in the US used Globus 4.0.4. Each of the nodes is equipped with a 1 Gbit/s Ethernet card and GRAPE-6Af hardware. Local nodes are interconnected with Gigabit Ethernet.

<table>
<thead>
<tr>
<th>name</th>
<th>country</th>
<th>CPU type</th>
<th>RAM [MB]</th>
<th>OS</th>
<th>kernel version</th>
</tr>
</thead>
<tbody>
<tr>
<td>vader</td>
<td>NL</td>
<td>Intel P4 2.4GHz</td>
<td>1280</td>
<td>Ubuntu 5.10</td>
<td>2.6.5</td>
</tr>
<tr>
<td>palpatine</td>
<td>NL</td>
<td>Intel P4 2.67GHz</td>
<td>256</td>
<td>RHEL 3</td>
<td>2.4.21</td>
</tr>
<tr>
<td>yoda</td>
<td>JP</td>
<td>Athlon 64 3500+</td>
<td>1024</td>
<td>FC 2</td>
<td>2.6.10</td>
</tr>
<tr>
<td>skywalker</td>
<td>JP</td>
<td>Athlon 64 3500+</td>
<td>1024</td>
<td>FC 2</td>
<td>2.6.10</td>
</tr>
<tr>
<td>obi-wan</td>
<td>US</td>
<td>2x Xeon 3.6GHz</td>
<td>2048</td>
<td>Gentoo 06.1</td>
<td>2.6.13</td>
</tr>
</tbody>
</table>

dominated by the force calculation rather than by communication. Earlier experiments indicate that a grid of regular PCs across Europe improves overall performance for relatively small $N$ [46]. We address the question for which problem size a world-wide grid has practical usage, in particular if such a cluster is equipped with GPUs or GRAPEs.

2.2 Experiment setup

We have constructed a heterogeneous grid of GRAPEs, which we call the Global GRAPE Grid (or G3). The G3 consists of five nodes across three sites. Two nodes are located at Tokyo University (Tokyo, Japan), two are located at the University of Amsterdam (Amsterdam, the Netherlands) and one is at Drexel University (Philadelphia, United States). Each of the nodes is equipped with a GRAPE-6Af special purpose computer, which allows us to test several different resource topologies. Local nodes are connected by Gigabit Ethernet, whereas the different sites are connected with regular internet. In Table 2.1 we present the specifications of the G3. Each of the computers in the G3 is set up with Globus Toolkit middleware\(^1\) and MPICH-G2\(^2\).

In Table 2.2 we present the network characteristics, latency and bandwidth, of the connections within G3. We tested local area network (LAN) and wide area network (WAN) connections using the UNIX \texttt{ping} command to measure latency. We use \texttt{scp} for measuring the network bandwidth, transferring a 75 MB file, rather than referring to theoretical limits because the majority of bandwidth on non-dedicated WANs is used by external users. For our performance measurements, we used a standard implementation

\(^1\)http://www.globus.org
\(^2\)http://www3.niu.edu/mpi/, in the future: http://dev.globus.org/wiki/MPICH-G2
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Table 2.2: Characteristics of local and wide network connections. Latency indicates the required time for sending 1 byte through the network connection. The bandwidth indicates the transfer capacity of the network connection. The bandwidth was measured with a 75MB `scp` file transfer.

<table>
<thead>
<tr>
<th>connection</th>
<th>latency [ms]</th>
<th>bandw. (theory) [MB/s]</th>
<th>bandw. (real) [MB/s]</th>
</tr>
</thead>
<tbody>
<tr>
<td>Amsterdam LAN</td>
<td>0.17</td>
<td>125.0</td>
<td>11.0</td>
</tr>
<tr>
<td>Tokyo LAN</td>
<td>0.04</td>
<td>125.0</td>
<td>33.0</td>
</tr>
<tr>
<td>Amsterdam - Tokyo WAN</td>
<td>266.0</td>
<td>57.0</td>
<td>0.22</td>
</tr>
<tr>
<td>Amsterdam - Phil. WAN</td>
<td>104.0</td>
<td>312.5</td>
<td>0.56</td>
</tr>
<tr>
<td>Philadelphia - Tokyo WAN</td>
<td>188.0</td>
<td>57.0</td>
<td>0.32</td>
</tr>
</tbody>
</table>

of MPICH-G2 without specific optimizations for long-distance networking. As a result, the MPI communication makes use of only 40%-50% of the available bandwidth. If we were to enhance MPICH-G2 with additional optimizations, or add support for grid security to already optimized MPI libraries, such as Makino’s tcplib or OpenMPI, our bandwidth use would be close to the bandwidth use of a regular file transfer.

The N-body integrator we have chosen for our experiments uses block time-steps with a 4th order Hermite integration scheme. The time steps with which the particles are integrated are blocked in powers of two between a minimum of $2^{-22}$ and a maximum of $2^{-3}$. During each time step, the codes perform particle predictions, calculate forces between particles and correct particles on a block of active particles. Particle corrections include updates of positions and velocities, and computation of new block time steps of particles.

For our experiments we use three implementations of a parallel N-body integrator. One of these codes runs on a single PC with and without GRAPE. The two others are parallelized using MPI: one of these uses the copy algorithm [83, 27] and the other uses the ring algorithm [33, 10, 46]. When using a copy algorithm, each process contains a copy of the full system, but integrates only a subset of the active particles. After integration, all active particles are exchanged between the processes to ensure that each copy of the N-body system is kept up-to-date. When using a ring algorithm, each process contains only a fraction of the full N-body system. Here, the active particles traverse across all processes in a ring topology and receive a partial update from each process. The copy algorithm has a smaller number of communication steps whereas the ring algorithm has lower memory usage on the nodes.

We initialize the simulations using Plummer [103] spheres that were in virial equilibrium and performed our simulations using a softening parameter of $2^{-8}$. Since our simulations are performed over one dynamical (N-body) time unit [54], the realization

\[^{3}\text{for more information we refer to a research report from INRIA: http://hal.inria.fr/inria-00149411/en/}\]

\[^{4}\text{see:http://grape.mtk.nao.ac.jp/~makino/softwares}\]
of the $N$-body system is not critical to the timing results.

2.3 Results of grid calculations

We have performed a number of simulations on local machines and on the G3, which consists of simulations lasting one $N$-body time unit and shorter simulations lasting one integration time step. We measured the full wall-clock execution time for the longer simulations and we profiled the shorter simulations.

2.3.1 Timing results of $N$-body calculations

We run the $N$-body codes, discussed in §2.2, on a single PC and across the network in parallel using $N = 1024$ to $N = 65536$ (a few additional calculations were performed with $N > 65536$). The runs were performed with and without GRAPE. In Figs. 2.1 and 2.2 we present the results of the copy and ring algorithms. If a simulation is run multiple times with the same problem set, the execution time may be slightly different per run. This variation is relatively small, as the slowest of 4 repeated runs (using 32768 particles over two sites) was a factor 1.07 slower than the fastest run. The variation can be primarily attributed to fluctuations in the network bandwidth.

Single PC

The performance on a single PC (represented by the thick solid line with bullets in Fig.2.1) is entirely dominated by force calculations, which scales as $O(N^2)$. As the number of steps per $N$-body time unit increases with $N$, the execution time scales slightly worse than $N^2$.

Grid of PCs

The performance on the G3, without using GRAPE, is given by the thin dashed line with triangles. For $N < 24576$, the performance is dominated by network communication. Given that $p$ indicates the number of processes, the network communication scales as $O(N \log p)$ (see [50]). For our grid-based simulation experiments without GRAPE, break-even between communications and force calculations is achieved around $N \sim 3 \cdot 10^4$ for the copy algorithm (Fig.2.1), and at a somewhat higher value for the ring algorithm (Fig.2.2). For larger $N$, the execution time is dominated by force calculations, rather than network communication. For these high $N$, the grid speedup $\Gamma$ [61], which is the single-site execution time divided by the execution time over three grid sites, increases to 1.37 for the copy and 1.24 for the ring algorithm. As can be seen by comparing Figs. 2.1 and 2.2, the copy algorithm gives overall better performance than the ring algorithm. This can be explained by the smaller number of communication steps in the copy algorithm.

Single PC with GRAPE

The performance on a single PC with GRAPE is dominated by force calculations, although communication between host and GRAPE, and operations on the host machine have an impact on performance for $N < 16384$. In addition, the GRAPE performs less efficiently for low $N$, because many blocks are too small to fill the GRAPE pipelines.
For larger $N$, force calculations become the performance bottleneck, and the scaling of the execution time becomes that of a single PC without GRAPE.

**Grid of PCs with GRAPE**

The performance on the G3 (with GRAPEs) using all three sites is given by the thin solid line with triangles. For all problem sizes $N$ we have measured, the grid speedup $\Gamma$ is less than 0.15, indicating that the performance is dominated by network communication. The network communication time scales better than the force calculation time, therefore, force calculation time will overtake the network communication time if $N$ is sufficiently large. However, this break-even point lies at much higher $N$ than for a Grid of PCs, because the use of GRAPE greatly decreases the time spent on force calculations.

For the copy algorithm (see Fig. 2.1), calculations between Tokyo and Philadelphia take less time than calculations between Amsterdam and Tokyo, due to a lower network latency (see Table 2.2). The calculations across three sites take more time than calculations across two sites. This is caused by the latency of all-to-all MPI communications in the copy algorithm, which scales with the number of processors.

According to our profiling measurements in Fig. 2.3, for $N < 12288$, a simulation on the G3 with GRAPEs using the ring algorithm spends most of its time in network latency. For larger $N$ more time is spent on using the network bandwidth. These results indicate that network bandwidth is the primary bottleneck for our simulations on the G3 using the ring algorithm. When we compare the results of the runs on the grid with GRAPEs with each other, we do not notice any systematic trend. The results confirm that the wall-clock time is dominated by using the network bandwidth, which is bottlenecked by the transpacific network line for all grid setups.

### 2.3.2 Profiling of the $N$-body simulations

We have chosen one parallel algorithm (ring) and one resource topology (3 nodes on 3 sites) to profile the simulation during one integration time step. The block size $n$ for every measurement was fixed using a formula for calculating average block size ($n = 0.20N^{0.81}$), which has been used for the same initial conditions in [108]. During execution, we measured the time spent on individual tasks, such as force calculations or communication latency between processes. We have profiled our simulations for $N = 1024$ up to $N = 196608$, using the timings measured on the process running in Tokyo. The results of these measurements are given in Fig. 2.3.

We find that for larger $N$, low bandwidth of our wide area network affects the outcome of the performance measurements, and that MPI calls are only able to use about a quarter of the available bandwidth for passing message content. For $N \gtrsim 5 \cdot 10^5$ we expect the force calculation to take more time than network latency. If we were to use the network bandwidth more efficiently for such a large number of particles, the execution time would be dominated by force calculations. The network bandwidth can be used much more efficiently, either by using a more efficient MPI implementation (e.g., one that supports communication over multiple TCP connections) or by using a dedicated network. Using our current networking and MPI implementation, we expect that for $N \gtrsim 2 \cdot 10^6$ particles the force calculation time overtakes the bandwidth time.
2.4 Modelling the performance of the grid

In order to further understand the results and to enable performance predictions for larger network setups, we decided to model the performance of the grid calculations. We model the performance of the simulation by adopting the parallel performance models described by [83] and [50] and combining it with the grid performance model described in [46]. Further extension and calibration of the model allows us to simulate the performance of our N-body simulations on a G3 or any other topology.
Figure 2.2: The time for running the application for 1 \(N\)-body time unit (\(T_{\text{app}}\)) as a function of the number of stars (\(N\)) for runs using the ring algorithm. See Fig. 2.1 for an explanation of the lines and symbols.

### 2.4.1 Single PC

An \(N\)-body simulation over one \(N\)-body time unit \([54]\) consists of the following steps:

1. Read the input snapshot and initialize the \(N\)-body system.
2. Compute the next system time \(t\) and select the block of \(n\) active particles in the system.
3. Predict the positions and velocities of all \(N\) particles to time \(t\).
4. Calculate the forces and their time derivatives between the \(n\) active particles and all \(N\) particles in the system.
5. Correct the positions, velocities and velocity derivatives of the \(n\) active particles, and update their time steps.
6. Repeat from step 2 until \(t\) has exceeded one \(N\)-body time unit.
7. Write the output of the simulation and terminate it.

As relatively little time is spent on program initialization and finalization, we focus on the time to integrate the system (\(T_{\text{integrate}}\)), which consists of the tasks performed in steps 2 to 5. Throughout this paper we use uppercase \(T\) to refer to the time spent
2.4 Modelling the performance of the grid

Figure 2.3: Share of wall-clock time spent on individual tasks during a single time-step. Solid lines indicate tasks performed on the local machine. The thick solid line with filled circles represents time spent on force calculations, and the thin solid lines give the result for time spent on communication between PC and GRAPE (open triangles), particle corrections (open circles) and particle predictions (open squares) respectively. Dotted lines indicate time spent on communication between nodes. The thin dotted line with asterisks indicates time spent on communication latency between nodes and the thick dotted line with solid squares indicates time spent on using the network bandwidth.

in $n_{\text{steps}}$ integration steps, and the lowercase $t$ for the time spent in a single step. The total execution time $T_{\text{integrate}}$ is

$$T_{\text{integrate}} = \sum_{i=1}^{n_{\text{steps}}} (t_{\text{pred}} + t_{\text{force}} + t_{\text{corr}}),$$

(2.1)

with the time spent on predicting particles

$$t_{\text{pred}} = \tau_{\text{pred}} n,$$

(2.2)

the time spent on calculating forces

$$t_{\text{force}} = \tau_{\text{force}} n N,$$

(2.3)

and the time spent on correcting the active particles

$$t_{\text{corr}} = \tau_{\text{corr}} n.$$

(2.4)
Table 2.3: Machine performance specification and machine-specific constants. The first two columns show the name of the machine, followed by the country of residence. The third column indicates machine speed in Mflop/s, using the Whetstone benchmark. The last three columns give the time required for the CPU to perform one particle prediction ($\tau_{\text{pred}}$), the time required for one force calculation between two particles ($\tau_{\text{force}}$) and the time required for correcting one particle ($\tau_{\text{corr}}$) respectively, all in microseconds.

<table>
<thead>
<tr>
<th>name</th>
<th>location</th>
<th>speed (Mflop/s)</th>
<th>$\tau_{\text{pred}}$ (µs)</th>
<th>$\tau_{\text{force}}$ (µs)</th>
<th>$\tau_{\text{corr}}$ (µs)</th>
</tr>
</thead>
<tbody>
<tr>
<td>vader</td>
<td>NL</td>
<td>377</td>
<td>0.247</td>
<td>0.216</td>
<td>4.81</td>
</tr>
<tr>
<td>palpatine</td>
<td>NL</td>
<td>422</td>
<td>0.273</td>
<td>0.193</td>
<td>2.39</td>
</tr>
<tr>
<td>yoda</td>
<td>JP</td>
<td>436</td>
<td>0.131</td>
<td>0.110</td>
<td>1.29</td>
</tr>
<tr>
<td>skywalker</td>
<td>JP</td>
<td>436</td>
<td>0.131</td>
<td>0.110</td>
<td>1.29</td>
</tr>
<tr>
<td>obi-wan</td>
<td>US</td>
<td>1191</td>
<td>0.098</td>
<td>0.148</td>
<td>1.14</td>
</tr>
</tbody>
</table>

Here $\tau_{\text{pred}}$ is the time to predict a single particle, $\tau_{\text{force}}$ is the time to calculate the forces between two particles, and $\tau_{\text{corr}}$ is the time spent to correct a single particle. The values for $\tau_{\text{pred}}$, $\tau_{\text{force}}$ and $\tau_{\text{corr}}$ have been measured by using a sample $N$-body simulation with 32768 particles, and are given in table 2.3 for the various nodes in the G3. For a more practical comparison, we also measured the compute speed (in floating point operations per second) for each of the nodes. These measurements were carried out using the Whetstone benchmark [23].

### 2.4.2 Grid of PCs with copy algorithm

The performance model for a single PC can be extended to include the parallel operation in the copy algorithm. In the copy algorithm, each process has a full copy of the system of $N$ particles, but only computes the active particles in a specific subset of $N/p$ particles. The result of this computation is sent to all other processes. We assume that all $p$ processes have comparable speed, and every process has an equally sized subset of $n/p$ active particles. For the copy algorithm, the host computation time ($T_{\text{integrate}}$) also consists of the time spent to communicate between processes ($T_{\text{MPI}}$). Therefore,

\[
T_{\text{integrate}} = \sum_{i=1}^{n_{\text{steps}}} (t_{\text{pred}} + t_{\text{force}} + t_{\text{corr}}) + T_{\text{MPI}},
\]

A process computes forces for its subset of $n/p$ active particles, and corrects only these particles. Therefore, a process requires at most $Nn/p$ force calculations per time step

\[
t_{\text{force}} = \tau_{\text{force}} \frac{N}{p},
\]

and a process corrects at most $n/p$ particles, of which the time spent is given by

\[
t_{\text{corr}} = \tau_{\text{corr}} \frac{n}{p}.
\]
In a parallel system, time is spent not only on integrating the system ($T_{\text{integrate}}$), but also on exchanging messages between processes ($T_{\text{MPI}}$). This time is obtained by adding the time spent on overcoming network latency ($t_{\text{latency}}$) and the time spent transferring particles ($t_{\text{bandwidth}}$)

$$T_{\text{MPI}} = \sum_{i=1}^{n_{\text{steps}}} (t_{\text{latency}} + t_{\text{bandwidth}}).$$

(2.8)

In our implementation $t_{\text{latency}}$ is given by the sum of the latencies of each MPI call in the code. The copy algorithm uses 1 MPI_Allgather and 1 MPI_Allgatherv command (of which the latencies both scale with $\log_2 p$ [50]) per block time-step, resulting in a total time spent on latency of

$$t_{\text{latency}} = (l_{\text{MPI_Allgather}} + l_{\text{MPI_Allgatherv}}) \log_2 p.$$  

(2.9)

The time used for transferring particle data is given by $t_{\text{bandwidth}}$, which is obtained by taking the total size of the data that has to be communicated (which is assumed to scale with $2(p-1)$ for all-to-all communications), and dividing it by the network bandwidth ($\tau_{\text{bw}}$). Particles are typically stored in a 124 byte data structure, which contains 4 arrays of 3 doubles each to store position, velocity, acceleration and derivative of the acceleration, 1 double each for mass, time step and last time step, and 1 integer for the identification number. This results in

$$t_{\text{bandwidth}} = \frac{n248(p-1)}{p\tau_{\text{bw}}}.$$  

(2.10)

For a wide area computer $t_{\text{latency}}$ and $t_{\text{bandwidth}}$ may be quite substantial, but the separation in parts, as given here, enables us to optimize our network computer with respect to the communication characteristics.

### 2.4.3 Grid of PCs with ring algorithm

Unlike the copy algorithm, the ring algorithm (discussed in detail in [33, 10]) does not use a single all-to-all communication operation, and only requires the processes to have a partial copy of size $N/p$ of the system. Communication occurs in a total of $p$ steps (or shifts). During every shift, each process performs a partial force integration by calculating the forces between their local subset of $n/p$ active particles and the $N/p$ particles stored in local memory. Then, each process sends its updated particles to their neighbor.

To model the performance for the ring algorithm, we use the model for the copy algorithm and redefine the time spent on force calculations ($T_{\text{force}}$) and the time spent to communicate between processes ($t_{\text{latency}}$ and $t_{\text{bandwidth}}$), as the force calculation and the MPI communications occur in multiple shifts. The time spent on a partial force calculation is given by

$$t_{\text{force,1shift}} = \tau_{\text{force}} \frac{nN}{p^2}.$$  

(2.11)
The total time spent on the force calculation is given by the time for a partial force calculation \((t_{\text{force,1shift}})\) multiplied by the number of shifts \((p)\),

\[
t_{\text{force}} = t_{\text{force,nN}} \frac{N}{p}.
\]  

(2.12)

For every time step, our implementation of the ring algorithm uses 2 MPI \texttt{Allreduce} communication commands for initialization, and 1 MPI \texttt{Sendrecv} operation for each shift. The time spent overcoming network latency is then

\[
t_{\text{latency}} = 2 \log_2 p \texttt{MPI\_Allreduce} + p \texttt{MPI\_Sendrecv}.
\]  

(2.13)

The ring algorithm is more bandwidth intensive than the copy algorithm, as all block subsets are sent and received during a ring shift, multiplying the time spent on transferring particles by 2\(p\). Per particle, 124 bytes have to be transferred, therefore the time spent on transferring particles is given by

\[
t_{\text{bandwidth}} = 248n / \tau_{\text{bw}}.
\]  

(2.14)

2.4.4 Single PC with GRAPE

The GRAPE-6Af is a dedicated hardware component developed by a group of researchers led by Junichiro Makino at the University of Tokyo [36]. The GRAPE-6Af is the smallest commercially available GRAPE configuration, consisting of a single GRAPE module with a peak speed of about 123 Gflop/s. It calculates the forces between particles, which is the bottleneck in the calculation, whereas the particle predictions and corrections are still mostly done on the host PC.

When a GRAPE is used, an \(N\)-body simulation over one \(N\)-body time unit consists of the following steps:

1. Read the input snapshot and initialize the \(N\)-body system.
2. Compute the next system time \(t\) and select the block of active particles \(n\) in the system.
3. Predict the positions and velocities of the \(n\) active particles on the PC, and send the predicted values and the next system time to the GRAPE.
4. Predict the other particles in the system on the GRAPE.
5. Calculate the forces and their time derivatives, using the GRAPE, between the \(n\) active particles and all \(N\) particles in the system.
6. Retrieve the forces and their time derivatives from the GRAPE.
7. Correct the positions, velocities and velocity derivatives of the \(n\) active particles, and update their time steps.
8. Repeat from step 2 until \(t\) has exceeded one \(N\)-body time unit.
9. Write the output of the simulation and terminate it.

When using GRAPE

\[ t_{pc} = t_{pred} + t_{corr}, \]  

(2.15)

where \( t_{pred} = n \tau_{pred} \). The time spent to integrate particles for one \( N \)-body time unit is given by

\[ T_{integrate} = \sum_{i=1}^{n_{steps}} (t_{pc} + t_{grape} + t_{comm}). \]  

(2.16)

Here

\[ t_{grape} = \tau_{pipe} nN, \]  

(2.17)

is the time for calculating the forces on the GRAPE. The time needed by the GRAPE to calculate the force between two particles is given by \( \tau_{pipe} \). The communication between host and GRAPE is given by [36]

\[ t_{comm} = 60t_i n + 56t_f n + 72t_j n. \]  

(2.18)

Here the time to transfer 1 byte of data to the GRAPE are given by \( t_i \) for the prediction step, \( t_f \) for the force calculation step, and \( t_j \) for the correction step. During these steps respectively 60, 56 and 72 bytes per particle in the block are transferred. We assume that \( t_i = t_f = t_j \). We derive \( t_j \) by measuring \( \tau_{Gsend} \), which is the time to send one 72-byte data unit to the GRAPE. Therefore, \( t_j = \tau_{Gsend}/72 \). By rewriting \( t_i \), \( t_f \) and \( t_j \) as factors of \( \tau_{Gsend} \), we can simplify the equation for \( t_{comm} \) to

\[ t_{comm} = (60 + 56 + 72) (\tau_{Gsend}/72) n. \]  

(2.19)

Time spent on calculating forces on the GRAPE \( (t_{grape}) \) cannot be directly measured by timing parts of the code, because the GRAPE force calculation includes some communication between host and GRAPE as well. However, we can derive \( \tau_{pipe} \) from the total time of the force calculation as was done in [50]. Therefore, we can rewrite \( \tau_{pipe} \) as

\[ \tau_{pipe} = \frac{1}{N} [t_{force} - (116\tau_{Gsend}/72)]. \]  

(2.20)

The time spent on performing \( N \) force calculations \( t_{force} \) is given by,

\[ t_{force} = \tau_{Gforce} N, \]  

(2.21)

where \( \tau_{Gforce} \) is the time spent to calculate forces between two particles. We then introduce the time constant \( (\tau_{Gforce}) \) in the function for \( \tau_{pipe} \),

\[ \tau_{pipe} = \tau_{Gforce} - \left( (116\tau_{Gsend}/72) \frac{1}{N} \right). \]  

(2.22)

Using our derived functions for \( t_{grape} \) and \( t_{comm} \), we are now able to model the performance of the GRAPE. As mentioned in [36], \( \tau_{Gforce} \approx 4.3 \cdot 10^{-10} \) s.
2.4.5 Grid of PCs with GRAPE and copy algorithm

When using GRAPE and a parallel algorithm, the time spent to integrate particles for one \(N\)-body time unit is given by

\[
T_{\text{integrate}} = \sum_{i=1}^{n_{\text{steps}}} (t_{\text{pc}} + t_{\text{grape}} + t_{\text{comm}}) + T_{\text{MPI}}. \tag{2.23}
\]

We determine time spent communicating between hosts \(T_{\text{MPI}}\) using Eq. 2.8. To determine the time spent on the host \(t_{\text{pc}}\) we use the equation for the single process with GRAPE (see Eq. 2.15). However, as we correct only \(n/p\) particles in parallel algorithms, we apply Eq. 2.7 to determine the time spent by the process on correcting particles.

In a parallel setup of GRAPEs, each process needs to communicate and calculate forces for a subset of \(n/p\) particle in every block. We replace \(n\) by \(n/p\) in our equations for \(t_{\text{grape}}\) as well as \(t_{\text{comm}}\). Therefore, the time spent on calculating forces is given by

\[
t_{\text{grape}} = \left[\frac{N\tau_{\text{Gforce}}}{p} - \frac{(116\tau_{\text{Gsend}}/72)}{p}\right] \frac{n}{p}, \tag{2.24}
\]

and communication between the hosts and the GRAPEs becomes,

\[
t_{\text{comm}} = (60 + 56 + 72) \frac{(\tau_{\text{Gsend}}/72)}{p} \frac{n}{p}. \tag{2.25}
\]

2.4.6 Grid of PCs with GRAPE and ring algorithm

In a ring algorithm, each process computes the forces between a local set of \(n/p\) active particles and the local system of \(N/p\) particles during a shift. Then, it sends the results to its next neighbor and receives another \(n/p\) particles from its other neighbor. Each of the nodes spends \(t_{\text{grape,1shift}}\) calculating the forces for \(n/p\) particles during one shift. Before the node has integrated the force on all \(n\) particles, a total of \(p\) shifts have passed, resulting in a total compute time for this node of

\[
t_{\text{grape}} = pt_{\text{grape,1shift}}, \tag{2.26}
\]

where the time to calculate forces for a single shift \(t_{\text{grape,1shift}}\) is

\[
t_{\text{grape,1shift}} = \left[\frac{(N\tau_{\text{Gforce}}/p) - (116\tau_{\text{Gsend}}/72)}{p}\right] \frac{n}{p}. \tag{2.27}
\]

When GRAPE is used, the exchanged particles are 172 bytes each because they contain two additional arrays for storing the old acceleration and the old derivative of the acceleration. Due to this increased particle size,

\[
t_{\text{bandwidth}} = 172n/\tau_{\text{bw}}, \tag{2.28}
\]

whereas \(t_{\text{latency}}\) remains unchanged. The time spent communicating between hosts \(T_{\text{MPI}}\) is calculated as for the ring algorithm without GRAPE.
2.5 Results of the performance model

We have applied the performance model from the previous section to the results presented in §2.3. In Fig. 2.1 we compare the measured wall-clock time ($T_{app}$) for the copy algorithm on the grid with the performance model, Fig. 2.2 shows a similar comparison for the ring algorithm. To guide the eye, the results for a single GRAPE are also presented in both figures. The performance model tracks the real measurements quite satisfactorily, giving a slightly lower computation time for a single GRAPE while giving a slightly higher computation time for a simulation across grid sites.

The communication overhead of a distributed computer often renders high performance computing on a grid inefficient. However, in the $N$-body problem the compute time scales with $N^2$ whereas the communication scales linearly with $N$. For sufficiently large $N$, there will eventually be a point where relatively little time is lost communicating, and the compute resources are efficiently used.

In figures 2.1 and 2.2 we can see that, for GRAPE-enabled simulations, break-even between calculation and communication is reached around $N \approx 10^6$. For large $N$, a grid of two GRAPEs will outperform a single GRAPE. Our grid setup included three GRAPE-enabled sites. The location of these sites (Asia, Europe and America) were as widely distributed as physically possible. A more modest grid across a single continent, will perform considerably better than a global grid. With the performance model that we constructed in §2.4, we can now study various grid topologies without the need to physically build the environment and create a virtual organization.

2.5.1 Future prospects

We applied the performance model to three hypothetical grids of GRAPE nodes. These three grids are: 1) a grid of all the available GRAPEs on the planet, 2) a grid of sites with more than 1 Tflop/s in GRAPE speed, and 3) a recently established Dutch grid (Dutch ASCII Computer, DAS-3\footnote{http://www.starplane.org/das3/}) equipped with GRAPEs.

Since the launch of GRAPE-6, a total of 1115 GRAPE-6 modules have been deployed worldwide. Japan leads the GRAPE-yard with more than 800 modules, followed by the US (119) and Germany (62). At the moment there are 876 GRAPE-6 modules in Asia, 132 in North America and 107 in Europe. In Table 2.4 we list the sites with more than 1 Tflop/s peak-performance in GRAPE hardware and their network characteristics. The network round-trip with the longest latency is a round-trip between Japan and Ukraine, whereas the network link with the longest latency (not given in Table 2.4) is the transpacific line between Japan and the US.

Organizing all the GRAPEs on the planet would be a challenging political problem. Organizing only the 13 largest sites would be somewhat easier, therefore we included a performance prediction of such an infrastructure as well. Constructing a virtual organization within the 4 universities (University of Amsterdam, Free University of Amsterdam, Leiden University and Delft Technical University) that participate in the DAS-3 project (and equipping the 270 available DAS-3 nodes with specialized hardware)
Table 2.4: Overview of the major GRAPE clusters (> 1Tflop/s) on planet Earth, including their relative network latency characteristics. The first column identifies the site, followed by the name of the institute, the country and the number of GRAPE modules (8 modules provide ~ 1 Tflop/s peak performance.). The fifth column identifies the site with which the latency, given in column #6, is shortest. The one but last column (column #7) identifies the site with which the latency, given in column #8, is longest. The total number of GRAPE modules is 996.

<table>
<thead>
<tr>
<th>ID</th>
<th>institution</th>
<th>country</th>
<th>GRAPE count</th>
<th>nearest site</th>
<th>farthest site</th>
</tr>
</thead>
<tbody>
<tr>
<td>B</td>
<td>Tsukuba Univ.</td>
<td>Japan</td>
<td>240</td>
<td>A 2 M</td>
<td>330</td>
</tr>
<tr>
<td>C</td>
<td>AMNH</td>
<td>US</td>
<td>40</td>
<td>G 5 B</td>
<td>190</td>
</tr>
<tr>
<td>D</td>
<td>Astr. Rechen Inst.</td>
<td>Germany</td>
<td>30</td>
<td>K 2 B</td>
<td>280</td>
</tr>
<tr>
<td>E</td>
<td>Rochester Inst.</td>
<td>US</td>
<td>26</td>
<td>F 5 B</td>
<td>170</td>
</tr>
<tr>
<td>F</td>
<td>McMaster Univ.</td>
<td>Canada</td>
<td>13</td>
<td>E 5 B</td>
<td>170</td>
</tr>
<tr>
<td>G</td>
<td>Drexel Univ.</td>
<td>US</td>
<td>12</td>
<td>C 5 B</td>
<td>190</td>
</tr>
<tr>
<td>H</td>
<td>Max Planck Inst.</td>
<td>Germany</td>
<td>12</td>
<td>D 10 B</td>
<td>290</td>
</tr>
<tr>
<td>I</td>
<td>Univ. of A'dam</td>
<td>Netherlands</td>
<td>10</td>
<td>K 7 B</td>
<td>266</td>
</tr>
<tr>
<td>J</td>
<td>Wien University</td>
<td>Austria</td>
<td>10</td>
<td>H 10 B</td>
<td>290</td>
</tr>
<tr>
<td>K</td>
<td>Bonn University</td>
<td>Germany</td>
<td>9</td>
<td>D 2 B</td>
<td>280</td>
</tr>
<tr>
<td>L</td>
<td>Cambridge Univ.</td>
<td>England</td>
<td>9</td>
<td>I 10 B</td>
<td>260</td>
</tr>
<tr>
<td>M</td>
<td>Main Astr. Obs.</td>
<td>Ukraine</td>
<td>9</td>
<td>J 30 B</td>
<td>330</td>
</tr>
</tbody>
</table>
would be much easier than doing this across various countries. The Dutch DAS-3 Grid is equipped with a fast Myrinet-10G internet and distributed across the Netherlands, connected by 10Gb light paths between clusters. The latency for the longest path is estimated to be 3 ms, and we estimate the bandwidth of the connections to be 0.5 GB/s.

In the sequential case we do not take memory limitations into account. This assumption is unrealistic for predicting the performance of a GRAPE, since \( N < 262144 \), but GPUs have a similar performance to GRAPE, and are able to store up to 13 million particles [108]. In late 2007, the launch of a double-precision GPU is expected, making GPUs usable for production-type direct-method \( N \)-body simulations. Additionally, in recent years the amount of memory on GPUs has been steadily increasing, and we expect this trend to persist in the near future.

We model the ring algorithm on both the G3 and on the 13 largest sites (see Table 2.4). We adopted *palpatine* (see Table 2.1) as the workhorse host for the GRAPEs and adopted the network characteristics as listed in Table 2.4. The ring algorithm for our hypothetical grid experiment was assumed to be optimized for the use of distributed clusters of GRAPEs. The algorithm avoids latency intensive network links by combining communication of local clusters from multiple shifts. Thus, communication for the local (across node) network is separated from the global (across sites) communication. Finally, we assume that all networks between the sites have reasonable support for MPI multicast and gather operations, and that the latency of these operations scales with \( \log_2 p \).

The results of the hypothetical global GRAPE grid are presented in Fig. 2.4. Here we see that a global grid in which all GRAPEs participate outperforms a single GRAPE by about two orders of magnitude for \( N \gtrsim 10^8 \) particles. For a sufficiently large number of particles (\( N \gtrsim 10^6 \)) the total peak performance of the global GRAPE grid approaches about 75 Tflop/s. Eventually, the grid with all the GRAPEs would outperform the grid with only the largest machines by about 25%, proportional to the number of GRAPEs in the two setups. When running simulations of \( N \sim 10^6 \) it is faster to run on three large GRAPE sites, than to use all the GRAPEs on the planet in parallel.

The dashed curve in Fig. 2.4 shows the performance of the model assuming that all the 270 nodes of the DAS-3 were equipped with GRAPE-6Af hardware. With such a setup, the maximum performance of about 35 Tflop/s is achieved for \( N \sim 10^7 \) particles. This is an interesting number for production simulations for astronomical research.

In Fig. 2.5 we present the wall-clock time for each of the different ingredients of a grid calculation with GRAPEs on the DAS-3, using the performance model. Break-even between calculation (straight solid curve) and communication (thick dotted curve) is achieved around \( N \sim 3 \cdot 10^6 \). For this large number of particles the communication between GRAPE and host, the predictor and the corrector steps require little CPU time compared to the force calculation on the GRAPE. For \( N \gtrsim 6 \cdot 10^6 \) this setup would give an efficient use of the special processors, and high performance calculations on the grid would then be quite efficient.
Figure 2.4: Speedup prediction of possible GRAPE grid setups compared to a single CPU. The solid lines indicate execution time using 1 CPU (horizontal reference line) or 1 GRAPE with infinite memory (curved line). The double-dotted line indicates the predicted speedup if all 1115 GRAPEs are linked together to perform one simulation using an optimized ring algorithm. The dashed line indicates the predicted speedup if all GRAPE sites with more than 1 Tflop/s are linked together to perform one simulation using an optimized ring algorithm. The dotted line indicates the speedup if all 270 nodes in the Dutch DAS-3 grid would be equipped with GRAPEs.

2.6 Discussion and conclusions

We studied the potential use of a virtual organization in which GRAPEs are used in a wide area grid. For this purpose, we developed a performance model to simulate the behavior of a grid in which each of the nodes is equipped with special purpose GRAPE hardware. We tested the performance model with an actual grid across three sites, each of which is located on a different continent. We used GRAPE hardware in Japan, the Netherlands and the USA simultaneously for calculations of 1024 up to 196608 particles.

With these particle numbers we were able to have a better performance than a single computer without GRAPE. We measured a grid speedup of $\Gamma \sim 1.37$ for a grid of PCs, and a grid of GRAPEs performs another $\sim 4$ times faster. On the entire range of $N$ we were unable to reach superior speed compared to a single GRAPE. However, we estimate that a small intercontinental grid of GRAPEs will reach superior performance for $N \gtrsim 3 \cdot 10^6$ particles.
Figure 2.5: Predicted decomposition of performance of a DAS-3 GRAPE grid. The thick solid line indicates total execution time and the flat thick dashed line indicates time spent due to network latency. The thin dashed line indicates time spent on using the network bandwidth and the steep thick dotted line indicates time spent on calculating forces. The three bottom lines indicate time spent on communication between hosts and GRAPEs (upper thick dash-dotted line), correcting particles (middle dash-dotted line), and predicting particles (lower dotted line).
We used our grid calculations with GRAPE to construct and calibrate a performance model, with which we studied the performance of a world-wide grid of GRAPEs. When all the GRAPEs on the planet would participate in a virtual organization it is possible to utilize the total machine’s performance, but only for really large systems of \( N \gtrsim 10^9 \). Though the total performance for such a setup would be about 75 Tflop/s, such large \( N \) would still be impractical to run for production astronomical simulations.

We conclude that organizing all the major GRAPEs on the planet in a virtual organization is probably not worth the effort. Organizing a few of the largest sites with GRAPEs within one continent, however, appears politically doable and computationally favorable. For the DAS-3, for example, the GRAPEs would be used at maximum performance for a feasible number of stars. Modern simulations of up to about a million stars have been done before using GRAPE [107], but these calculations were performed on a single cluster, rather than on a grid. A grid setup as proposed here would allow the simulation of a few million stars within a reasonable time span.

If we were to equip the full DAS-3 wide area computer in the Netherlands with GRAPEs, maximum performance would already be achieved for \( N \sim 6 \cdot 10^6 \) particles. Though still large, such simulations would be very doable and have practical applications. We estimate that running a system of \( N = 10^6 \) stars with a Salpeter mass spectrum [115] over a wide range of stellar masses to the moment of core collapse would take about 4 months. The simulation would still be mostly dominated by network latency, but the high-throughput networking in the DAS-3 completely removes the bandwidth bottleneck.

We have mainly discussed the use of GRAPEs in a virtual organization, but new developments in using graphical processing units appear to achieve similar speeds as GRAPEs [108, 49, 18]. In addition, GPUs are equipped with a larger amount of memory, which allows us to exploit more memory-intensive, but also faster, parallel algorithms. Future grids are likely to be equipped with GPUs, as the GPU will become part of the standard equipment for every PC.

Although our proof-of-concept infrastructure was of limited size, we have shown that it is possible to use dedicated hardware components located across clusters for high-performance computing. Though the current performance over globally connected grids leaves a lot to be desired and much optimization remains to be done, the concept of using dedicated hardware components worldwide in parallel has been shown to work. It can therefore be applied to solving individual tightly-coupled scientific problems or as ingredient of a complex multi-physics simulation, such as simulating a full galaxy, given that the problem size is sufficiently large to overcome the networking limitations.