Tools and techniques for efficient system-level design space exploration
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Scientific output

Sesame, Daedalus and Case-studies


Model Calibration


**Modeling Dynamically Reconfigurable Systems**

• K. Sigdel, C. Galuzzi, M. Thompson, A.D. Pimentel, K. Bertels, *Runtime Task Mapping Based on Hardware Configuration Reuse*, in the Proceedings of the International Conference on ReConFigurable Computing and FPGAs (ReConFig ’10), Cancun, Mexico, December 2010


**Multi-application Modeling**

Other Tools and Techniques


