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Test beam results from the prototype L3 Silicon Microvertex Detector

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We report test beam results on the overall system performance of two modules of the L3 Silicon Microvertex Detector exposed to a 50 GeV pion beam. Each module consists of two AC coupled double-sided silicon strip detectors equipped with VLSI readout electronics. The associated data acquisition system comprises an 8 bit FADC, an optical data transmission circuit, a specialized data reduction processor and a synchronization module. A spatial resolution of 7.5 μm and 14 μm for the two coordinates and a detection efficiency in excess of 99% are measured.

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1. Introduction

A Silicon Microvertex Detector (SMD) has been recently installed as the inner most position of the central tracker of the L3 experiment at the CERN LEP storage ring.

Sectional views of L3's tracking system are shown in the upper part of Fig. 1. A high resolution drift chamber working in the time expansion mode [1] (TEC) provides multiple coordinate measurement in the Φ plane. Two multiwire proportional chambers with cathode strip readout [2] are placed at the outer radius of the TEC and deliver two Z -coordinate measurements. As can be seen from the lower part of Fig. 1 the SMD consists of two cylindrical shells of double-sided silicon microstrip sensors surrounding the beam pipe at radii of about 6 cm and 8 cm. The sensors measure the spatial coordinates of charged particles over the polar angle range $22^\circ \leq \theta \leq 158^\circ$ and over the full azimuth. A detailed description of the SMD design can be found in ref. [3].

Precision measurements on charged tracks close to the interaction point will improve L3's current momentum, impact parameter and polar angle resolutions, essential for b-quark tagging and W-boson studies at LEP200.

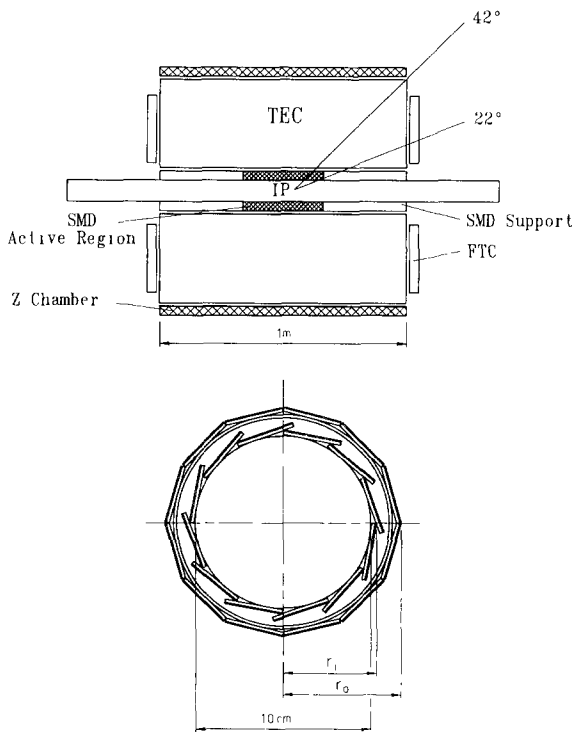


Fig. 1. Sectional views of the tracking system of the L3 detector

Our test beam experiment was performed to gain experience operating the complete system of sensors, front-end electronics, cooling system, position monitoring systems and data acquisition chain. We measured the spatial resolution, detection efficiency, cluster size distribution and signal to noise ratio of the silicon sensors.

2. The prototype detector

Two modules of the SMD were installed during summer 1992 in a test beam of 50 GeV pions. For the first time, prototypes of all components of the system, corresponding to 1/24 of the actual SMD, were set up. We now describe the main components.

2.1. The detector modules

Fig. 2 shows a detector module. A module consists of 2 consecutive silicon sensors of planar size $\sim 7 \times 4$ cm² and 300 μ m thickness. The Φ -measuring strips run the full length on one side of the module while on the other side are orthogonal Z -measuring strips.

The sensors are fabricated [4] on a design by INFN Pisa [5] from n -doped silicon wafers of resistivity ≥ 6 k Ω cm and fully depleted between 30 and 50 V. The implantation strip pitch is 25 μ m on the diode (Φ -measuring) side and 50 μ m on the ohmic (Z -measuring) side. The readout pitch is 50 μ m on the Φ -measuring side and, depending on the polar angle, 150 or 200 μ m on the Z -measuring side. A detailed description can be found in ref. [6].

The readout electronics for both sensor sides are placed at the same end of the module. The Φ -side readout strips are AC-coupled to the preamplifiers by a special integrated 150 pF capacitor built on a quartz substrate.

The signals from the Z -side strips are transmitted to the AC coupling capacitors of the readout electronics by a special flex cable made from 2.5 μ m thick, L-shaped copper strips plated onto a 50 μ m thick Kapton foil [7].

2.2. The readout electronics

The strips on both sides of the sensors are read out by an electronics chain consisting of charge sensitive preamplifiers, an auxiliary electronics board, an analog to digital converter and an optical transmission line to the data acquisition system. A special synchronization module receives external triggers and synchronizes the components of the electronics chain (Fig. 3).

At the end of each module farthest from the interaction point, a hybrid carrying six SVX-H chips is glued to each side of the module (Fig. 2). The SVX-H

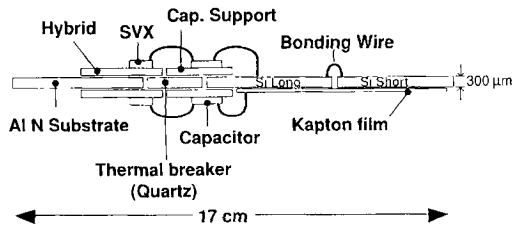


Fig. 2. Schematic view of the SMD module as used in the test beam.

is a specially developed VLSI radiation hard chip [8] fabricated using a $1.2 \mu\text{m}$ CMOS technology [9]. The chip comprises an analog and a digital section. The analog section contains 128 channels of low-noise charge sensitive amplifiers with a gain of $\sim 19 \text{ mV/fC}$. The digital section part controls the multiplexed readout and loads channel and chip identification onto a digital bus. A detailed performance study of the SVX-H chips can be found in ref. [10].

The six SVX chips on each hybrid are daisy-chained and read out by an intermediate electronics board, the “Converter”, placed near the detector. The Converter contains an amplifier to drive the analog data, low drop voltage regulators for the SVX analog and digital power and bidirectional line drivers for the SVX control and digital bus.

Analog and digital data are received from and sent to a special optical transceiver board (Optoboard) [11] placed at a 10 m distance from the detector. On the Optoboard the analog signals are digitized by an 8 bit 40 MHz flash ADC. All output data are sent by a 120 m optical fiber to an optical receiver at the front-end of the data acquisition system. The optical transmission decouples the detector and readout electronics from the data acquisition. This is done because the detector’s Φ and Z sides are at different potentials and to reduce ground loop problems.

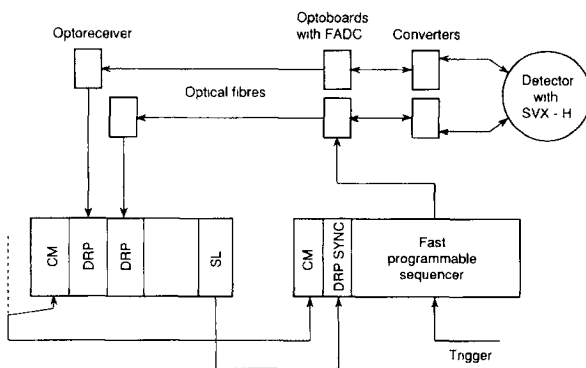


Fig. 3. Readout and data acquisition scheme.

2.3. The data acquisition system

The Data Acquisition System (DAQ) of the SMD (Fig. 3) is a branch of VME crates. Each crate is controlled by a Crate Master [12] which communicates with special Data Reduction Processors (DRPs) [13] in its crate and transfers their data to the data acquisition (DAQ) stream. The DRPs contain a processor chip and random access memory for programming, data and event buffering. Each DRP receives data from one daisy chain of up to 12 SVX chips and writes it to a fast memory.

The programs [14] running on the DRP handle different output modes. We used the raw mode in which the ADC contents of all channels of a SVX chain are transferred to the DAQ stream.

The synchronization of the readout to the external trigger is achieved by the Sequencer [15] which generates bit patterns necessary to start and synchronize the multiplexed readout of the SVX to the DRPs. Its central part is a micro-processor and a fast RAM. The memory contains the instructions and the output patterns which are sent to both the optoboard and the VME bus. The Sequencer generates patterns for pedestal taking, calibration and data taking.

A VAX cluster was used to assemble and download programs to the different levels of the DAQ system. It received and wrote data to a tape unit and served as the user front end.

3. Displacement monitoring systems

The real SMD has two independent systems to measure the relative angular and translational displacements of the SMD with respect to the rest of the L3 central tracker. One system is a laser displacement monitoring system (LDMS) and the other is a capacitive displacement monitoring system (CDMS). To test the operation of these systems during data taking we installed prototypes of the LDMS and CDMS along with our prototype detector modules.

The LDMS consists of a laser diode, optical fibers and lenses. The laser diode produces short pulses of infrared (905 nm) light transmitted by an optical fiber to a lens–prism assembly which finally directs the light onto the silicon sensors, illuminating several adjacent strips. The strips are then read out by the standard DAQ system. A change in the centroid of the light spot indicates a relative motion of a sensor with respect to the external mechanical frame holding the lens–prism assembly.

The CDMS monitors long term radial and transverse displacements. A single channel utilizes a sensor [16], mounted on the detector mechanical frame, facing a grounded electrode placed on the external support.

The sensor is instrumented by electronics which (a) excites the sensor with an AC current of fixed frequency and constant magnitude and (b) measures and processes the potential difference between the sensor and ground target to produce an essentially DC output voltage which varies inversely with the capacitance between sensor and ground electrode. This voltage is digitized by a CAMAC ADC/MUX system [17] and read out once per minute.

4. Cooling

The 10 μm design tolerance on positional errors for the actual SMD requires a very stable temperature. Consequently, we have cooled the prototype detectors in the same manner as foreseen in L3 and monitored their temperature stability. The total power dissipated by both the Converters and the SVX-H chips is about 0.5 W/cm^2 , approximately 15 W for both test modules.

The Converters were cooled by placing thin water cooled plates 1 mm above them and flowing 10 l/h of water through narrow aluminum pipes attached to the plates. The hybrids holding the SVX chips were cooled by flowing 10 l/h of water through a channel machined into the aluminum mechanical flange supporting the hybrids. All cooling circuits were under partial vacuum to prevent fluid loss if a leak developed.

The temperature of the front end electronics and the sensors was monitored through platinum wire resistance thermometers glued to the mechanical support structure of the modules.

5. Test beam set-up

Two modules, fully equipped with readout electronics, were installed in the X-3 test beam line at CERN. The modules were irradiated with 50 GeV pions during spills of 2.5 s duration, producing about 5 triggers per spill, roughly uniform in time. More than 10^5 triggers were written to tape at incident polar angles of 0° , 10° , 20° and 30° .

The overall scheme of the test setup is shown in Fig. 4. Two modules were placed on a replica of the real SMD mechanical structure. The modules had a relative position corresponding to an azimuthal wedge of the actual SMD. The replica is positioned inside a mock-up of the TEC. The mock-up could be rotated with respect to the beamline to measure detector performance as a function of the polar angle θ . It also held portions of the two displacement monitor systems.

Upstream and downstream of the modules were trajectory defining reference silicon strip detectors. These single-sided detectors had an active area be-

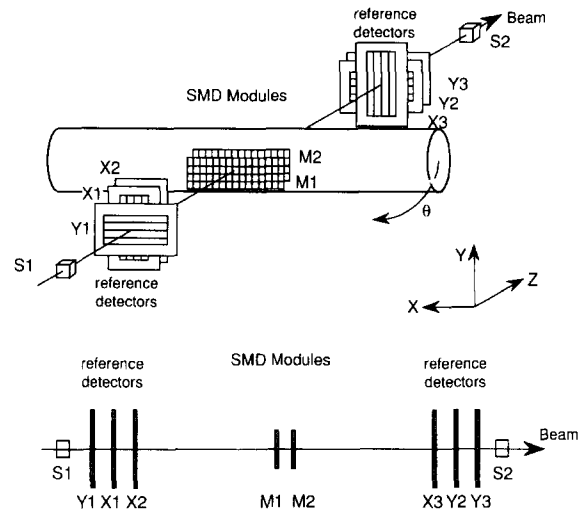


Fig. 4. The test beam set-up.

tween $13 \times 13 \text{ mm}^2$ and $16 \times 60 \text{ mm}^2$, a readout pitch of either $50 \mu\text{m}$ or $60 \mu\text{m}$ and measured positions along two axes perpendicular to the beam axis. They were read out by SVX-D chips and had their analog signals digitized by a 6 bit FADC and then transferred to a DRP.

A beam particle trigger was formed by two pairs of scintillator paddles, one pair upstream and one pair downstream of the modules under study. The DAQ trigger was the logical AND of all four scintillators.

6. Offline analysis and results

All components of the data acquisition system were running in raw data mode. Hence, the calculation of pedestals and their rms values, the selection of valid clusters, the calculation of hit coordinates and the determination of efficiency and spatial resolution were all done offline [18]. Data from the LDMS, the CDMS and the cooling system were also recorded.

We start the analysis by first determining, on an event by event basis, the significant common mode noise observed on all channels of a given SVX. Here, the common mode noise is defined to be the average pulse height per event per SVX, care being taken to exclude non-working channels and possible signals from the average. Next, this common mode noise is subtracted from the pulse height of each channel and the residual is then averaged over 100 triggers to obtain the pedestal and its root mean square for each channel. As can be seen from Fig. 5 we observed over periods of 16 hours a pedestal variation greater than its rms. We therefore applied an event by event adjust-

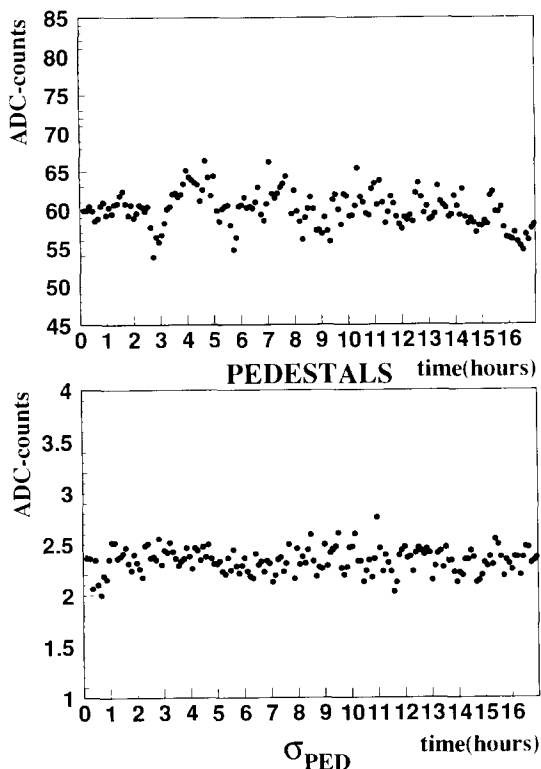


Fig. 5. Pedestal and σ_{ped} behavior over 16 hours.

ment [19] of the pedestals to account for slow coherent pedestal drifts.

6.1. Charge cluster and signal to noise ratio

Various selection criteria were applied in order to separate signals from noise. The following procedure gave the best results. For every trigger, common noise and pedestals were subtracted from the raw ADC values for each channel of a silicon layer. The residuals of each channel are scanned for signals larger than 5 times the rms of the channel pedestal. If this holds for a strip k , a charge cluster is defined to include all neighbors strips, provided their residual ADC content is larger than $2.5\sigma_{k \pm i}$. One more neighbor strip on each side is included if its residual ADC is larger than $1.5\sigma_{k \pm (i+1)}$. Only clusters with less than six strips went to the further analysis. A cluster is not formed if it would include a dead channel.

A qualitative justification of this algorithm is illustrated in Fig. 6. The scatter plot of the measured cluster charges belonging to the same trigger Q_Φ and Q_Z from the Φ and Z layers, respectively, shows a linear correlation between the two charges ($\sigma_{\text{cor}} = 19\%$). This is expected for double-sided sensors since a charged particle traversing the silicon sensor generates equal numbers of holes and electrons.

The distribution of the number of strips belonging to a cluster is shown in Fig. 7 for the Φ and Z layers for tracks which cross the modules at normal incidence. With the cluster definition as described above the average values for the cluster sizes are 3.1 and 2.3, respectively.

The dependence of the cluster size on the polar angle of the crossing track is shown in Fig. 8 for the Z layer. The observed increase with polar angle is expected since the projection of an incident particle's track onto the Z -side strips increases with increasing polar angle θ . As expected no increase in the cluster size on the Φ side was found. The errors are statistical ones.

The signal to noise ratio S/N is an important quality measure of a module. We define it as $S/N = Q_{\text{mp}}/\sigma_{\text{mean}}$, where Q_{mp} is the most probable value of the cluster charge and σ_{mean} is the mean value of the pedestal rms for the strips comprising the cluster.

The distributions of the total cluster charge for the Φ and Z layers are shown in Fig. 9. They qualitatively display a Landau shape. To determine Q_{mp} , fits to a convolution of a Landau distribution with a Gaussian distribution are performed. The resulting values for Q_{mp} are 43.8 and 43.2 ADC counts. With σ_{mean} values of 2.5 and 2.8, the S/N is 17.5 and 15.5 for the Φ and Z layers, respectively.

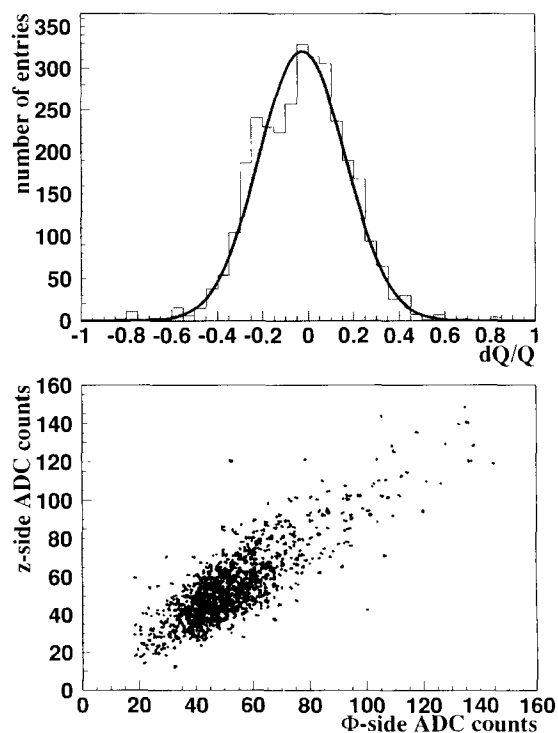


Fig. 6. Charge correlations between Φ and Z sides.

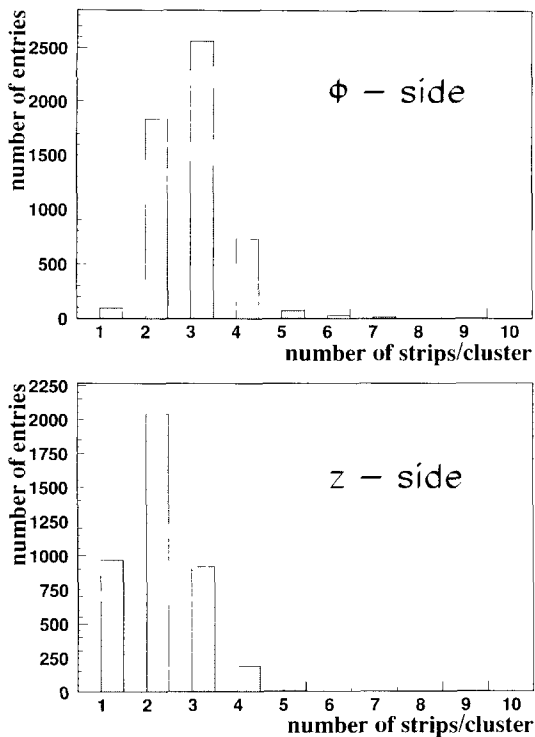
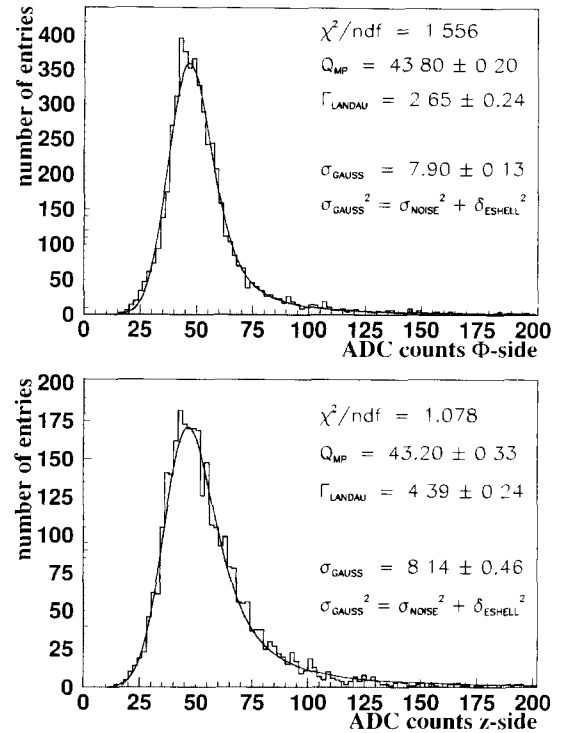
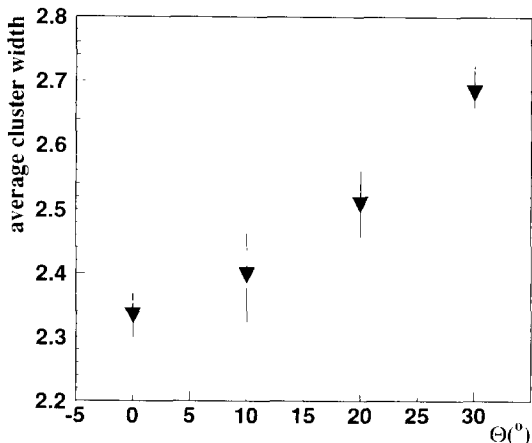


Fig. 7. Number of strips per cluster.

Fig. 9. Distribution of the total cluster charge for Φ and Z sides fitted by a convolution of a Landau and a Gauss function.

6.2. Spatial resolution

The impact point of a beam particle is defined by the cluster position, a rough estimate of which is simply the cluster center of gravity. However, the charge deposited in the silicon is collected by the two nearest strips in a non-linear way depending on the relative position of the impact point [20]. We define the param-

Fig. 8. The average Z side cluster size as a function of the polar angle θ .

eter $\eta = Q_l/(Q_l + Q_r)$, where Q_l and Q_r are, respectively, the charges collected on the left hand and right hand nearest strips.

Since the beam spot size is much larger than the readout strip pitch, we assume a uniform irradiation of the strips and correct the hit position by constructing a rectifying function from the cumulative integral of $dN/d\eta$, where N is the number of counts whose charge is divided among the two strips.

A particle track is defined by the hits in the reference detectors upstream and downstream of the prototype detector. The spatial resolution of the prototype is determined by comparing the coordinates predicted by the reference detectors with those measured in the prototype detector. The distributions of the differences $\delta\Phi = \Phi_{\text{ref}} - \Phi_{\text{meas}}$ and $\delta Z = Z_{\text{ref}} - Z_{\text{meas}}$ are then fitted by a Gaussian distribution, as shown in Fig. 10. Subtracting in quadrature the estimated resolution of the reference detectors yields a spatial resolution for the prototype Φ and Z layers of $\sigma_{\Phi} = 7.5 \mu\text{m}$ and $\sigma_z = 14.3 \mu\text{m}$, respectively, for non-inclined tracks.

The dependence of σ_z on the polar angle θ is shown in Fig. 11. The resolution deteriorates for inclined tracks due to the increase in geometrical projection of the incident particle's track onto the Z side sensor plane. The resolution on the Φ side as expected

does not depend on the polar angle of the track and is for all angles in the order of $8 \mu\text{m}$.

6.3. Detection efficiency

To calculate the detection efficiency ϵ , we consider a particle detected if in the corresponding detector layer a cluster is found at a position within $\pm 3\sigma_{\phi,Z}$ of the predicted one. In this case $\epsilon = 97\%$ for the Φ layer and $\epsilon = 96\%$ for the Z layer. If instead the cluster definition is changed to the requirement that a signal in some single channel be larger than 5 times the rms of the channel pedestal or two consecutive strips have a signal of more than 3 times the rms of their channel pedestal and that this signal be within four strips of the predicted impact point, the efficiency increases to $\epsilon = 99.9\%$ for the Φ layer and $\epsilon = 99.3\%$ for the Z layer.

6.4. Performance of the displacement monitoring systems

The possible effect of the CDMS on SMD noise levels was measured by taking a sequence of SMD pedestal runs with the CDMS alternately switched on and off. The centroids and shapes of the pedestal distributions for the runs with the CDMS on were

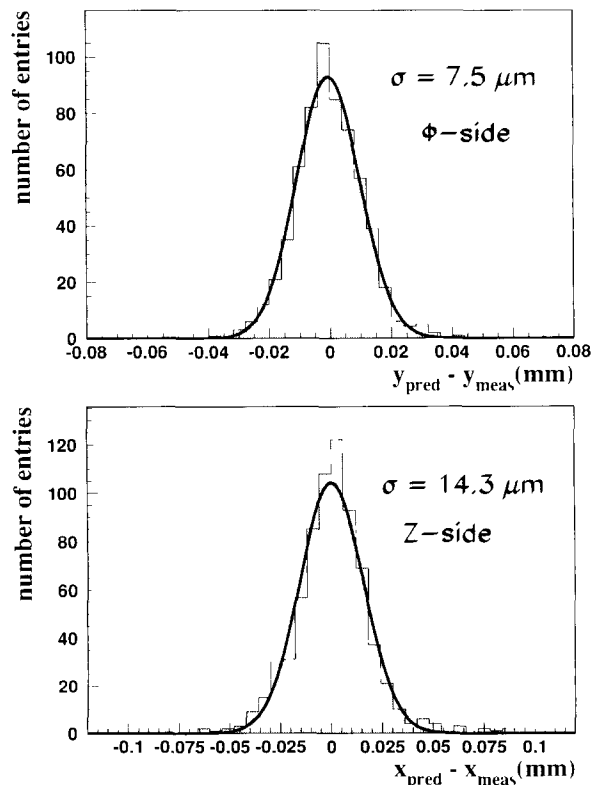


Fig. 10. Resolution of the prototype module for non-inclined tracks.

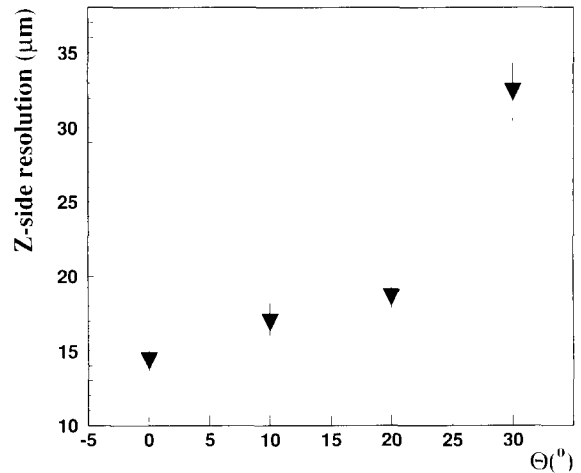


Fig. 11. The Z coordinate resolution as a function of the polar angle Θ .

found to be practically identical to those observed for runs with the CDMS off. No noise induced in the SMD data by the CDMS was observed. The short-term behavior of the CDMS in the test beam environment is consistent with that observed in laboratory bench studies.

Laser light from the LDMS was successfully observed on the Φ -measuring strips. The centroid of the light spot was stable to $\sim 3 \mu\text{m}$ during the data taking period.

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