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High Performance
Reconfigurable Computing with
Cellular Automata

High Performance Reconfigurable Computing with Cellular Automata

ACADEMISCH PROEFSCHRIFT

ter verkrijging van de graad van doctor
aan de Universiteit van Amsterdam
op gezag van de Rector Magnificus
prof. dr. D.C van den Boom
ten overstaan van een door het college voor promoties
ingestelde commissie, in het openbaar te verdedigen
in de Agnietenkapel
op dinsdag 25 mei 2010, te 10:00 uur

door

Syed Murtaza

geboren te Kashmir, India

Promotor: prof. dr. P.M.A. Sloot

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prof. dr. R.J. Meijer

Faculteit: Faculteit der Natuurwetenschappen
Wiskunde en Informatica



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Advanced experiments of the work were carried out using Maxwell – a 64-FPGA Super-computer, at EPCC, University of Edinburgh. The author also worked at EPCC on a one month fully funded *visiting academic programme* through the *FPGA High Performance Computing Alliance*.

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