



UvA-DARE (Digital Academic Repository)

High performance reconfigurable computing with cellular automata

Murtaza, S.

Publication date
2010

[Link to publication](#)

Citation for published version (APA):

Murtaza, S. (2010). *High performance reconfigurable computing with cellular automata*.

General rights

It is not permitted to download or to forward/distribute the text or part of it without the consent of the author(s) and/or copyright holder(s), other than for strictly personal, individual use, unless the work is under an open content license (like Creative Commons).

Disclaimer/Complaints regulations

If you believe that digital publication of certain material infringes any of your rights or (privacy) interests, please let the Library know, stating your reasons. In case of a legitimate complaint, the Library will make the material inaccessible and/or remove it from the website. Please Ask the Library: <https://uba.uva.nl/en/contact>, or a letter to: Library of the University of Amsterdam, Secretariat, Singel 425, 1012 WP Amsterdam, The Netherlands. You will be contacted as soon as possible.

Publications

1. S. Murtaza, A.G. Hoekstra, and P.M.A. Sloot, "Performance of Cellular Automata Simulations on a FPGA cluster," 2010, Submitted.
2. S. Murtaza, A.G. Hoekstra, and P.M.A. Sloot, "Performance of floating-point based Cellular Automata Simulations using a dual FPGA system," 2009, Submitted.
3. S. Murtaza, A.G. Hoekstra, and P.M.A. Sloot, "Compute Bound and I/O Bound Cellular Automata Simulations on FPGA logic," in *ACM Transactions on Reconfigurable Technology and Systems*, vol. 1, nr. 4, pp. 1–21. ACM, New York, January 2009.
4. S. Murtaza, A.G. Hoekstra, and P.M.A. Sloot, "Floating point based Cellular Automata simulations using a dual FPGA-enabled system," in *Proceeding of the Second International Workshop on High-Performance Reconfigurable Computing Technology and Applications (HPRCTA '08)*, held in conjunction with SC08, pp. 1-8. IEEE, Austin, Texas, November 27–29, 2008.
5. S. Murtaza, A.G. Hoekstra, and P.M.A. Sloot, "Performance modeling of 2D Cellular Automata on FPGA," in *17th International Conference on Field Programmable Logic and Applications (FPL'07)*, pp. 74–78. IEEE, Amsterdam, August 27–29, 2007.
6. S. Murtaza, A.G. Hoekstra, and P.M.A. Sloot, "Performance evaluation of FPGA-based Cellular Automata accelerators," in *Proceedings of the Third Annual Reconfigurable Systems Summer Institute (RSSI'07)*, (on line proceedings) +7. Reconfigurable Systems Summer Institute, National Center for Supercomputing Applications, Urbana, Illinois, July 17–20, 2007.